

Effects of Pervasive Computing on Sustainable Development

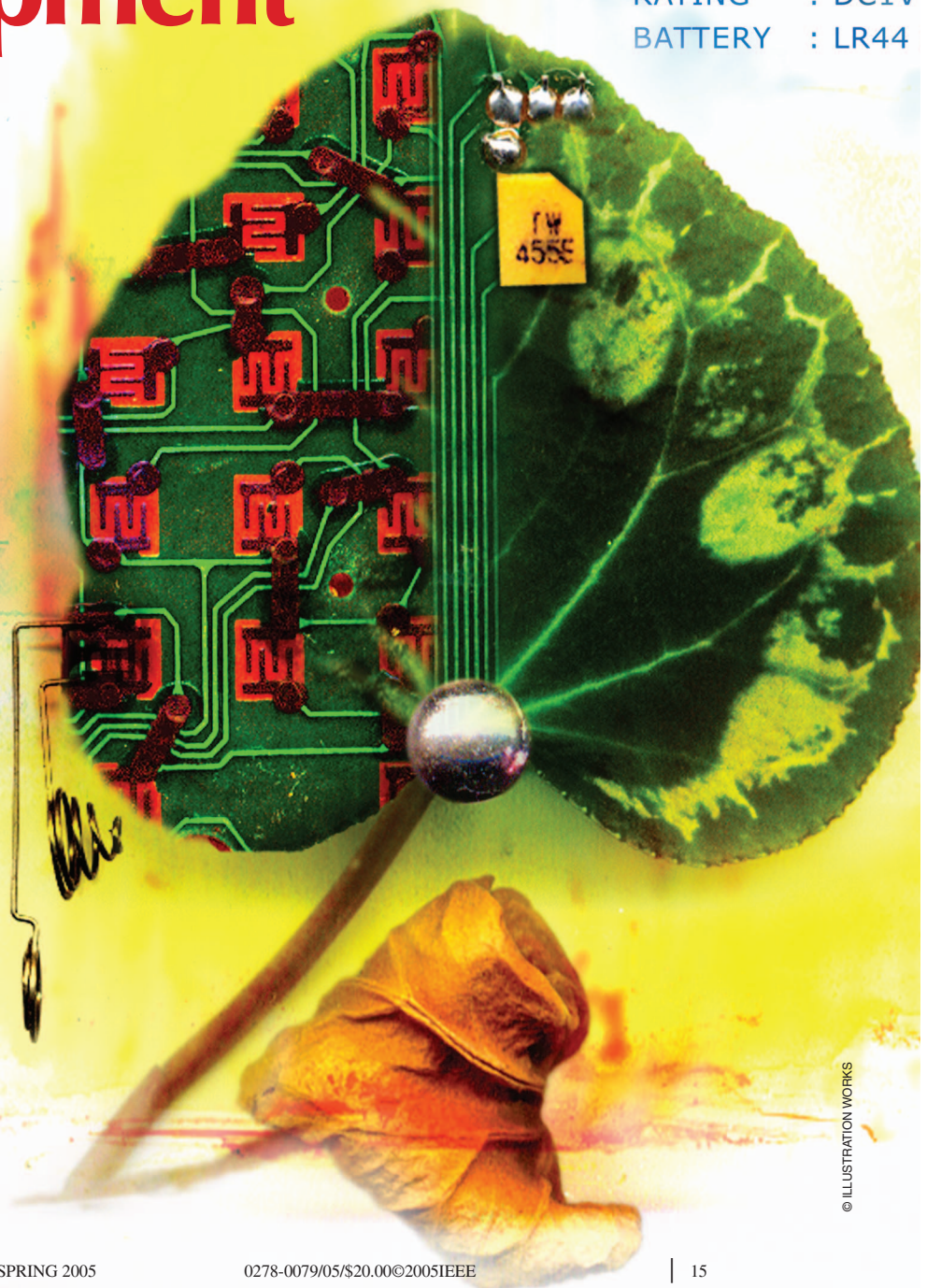
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Since pervasive computing is expected to become ubiquitous in the coming years, the question arises whether it will support sustainable development or may counteract that aim. Based on the results of a technology assessment study we show that pervasive computing could amplify already existing problems related to the environment, human health, and society. Power consumption for digital networks, e-waste streams, and exposure to non-ionizing radiation may all increase. Furthermore, social sustainability could be threatened by the technology if it is applied in a way that restricts consumers' privacy and freedom of choice. We refer to the precautionary principle as an analytical framework for discussing the opportunities and risks of pervasive computing for sustainable development.

Visionary New Ways to Apply ICT

Pervasive computing refers to visionary new ways of applying information and communication

MODEL NO.: LE45-
RATING : DC1V
BATTERY : LR44



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technologies (ICT) to our daily lives. It involves the miniaturization and embedding of microelectronics in non-ICT objects and wireless networking, making computers ubiquitous in the world around us [1]. The miniaturization of semiconductor technology is bound to continue for about another 10 years without breaking the

future generations to meet their own needs” [5]. This definition can be interpreted as an extension of the ethical principle of justice in space and time, leading to the principles of intra- and intergenerational fairness. It implies keeping as much space for future decisions open as possible, since we cannot anticipate today what the needs of future generations

fusion of this embedded and networked technology is practically irreversible due to costs of change and may therefore limit future development. Thus early precautionary measures could avert irreversible negative developments cost-effectively. In this way the precautionary principle can serve as a framework to constructively set the course for innovations [11]–[15].

The Swiss Centre for Technology Assessment (TA-SWISS) commissioned a study to display the opportunities and risks of pervasive computing, focusing on its impacts on human health and the environment. This article provides an overview of the most important results of that study [16].

Pervasive computing refers to visionary new ways of applying information and communication technologies (ICT) to our daily lives.

trend [2]. Further development of wireless communications by means of mobile phone networks and wireless local area networks (WLAN) will play a decisive role. It is likely that the number of mobile ICT components per person will rise into the hundreds. Unlike most of today’s ICT products, pervasive computing components will be equipped with sensors enabling them to collect data from their surroundings without the user’s active intervention. On the software level, so-called agent technologies will gain in importance.

If our life is to be pervaded in such ways by microelectronic components, running all the time with most of them wirelessly networked, one must ask whether these technologies might not have undesirable side effects that counteract the aim of a sustainable information society [3], [4]. In a sensible discussion about the effects of pervasive computing on sustainable development the expected benefits need to be weighed against the potential risks involved.

Our understanding of sustainable development is based on the so-called Brundtland definition: “A development is sustainable if it meets the needs of the present without compromising the ability of

will be. This is the aim of the precautionary principle [6], [7], which is recognized as one framework for sustainable development. The precautionary principle is intended to anticipate and minimize potentially serious or irreversible risks under conditions of uncertainty [8], [9].

Technology assessment for pervasive computing that is oriented towards sustainable development should, therefore, pay attention to trends that may be socio-economically irreversible [10]. In a theoretical sense, the concept of irreversibility can only be based on natural science. However the impacts of a novel technology on society may cause a situation in which it is practically impossible to restore the status before the addressed effect of the technology has occurred.

Irreversibility based on cultural facts should not be underestimated. As the diffusion of pervasive computing is coupled with considerable investments, it will be very difficult to change at a later date.

In order to make precautionary measures effective, risk assessment has to be started as early as possible and is to be accomplished before such technologies have penetrated the market. This is especially true for pervasive computing, as the dif-

Technology Assessment Study Approach

Pervasive computing comprises a broad and dynamic spectrum of technologies and applications. The assessment of long-term effects has to deal with uncertainty for two reasons. First, scientific knowledge – for example causal models of the long-term health effects of non-ionizing radiation (NIR) – is insufficient. Second, it is an open question as to how pervasive computing will develop in its various fields of application. We cannot predict how fast and to what extent the technology will be taken up and how it will be used.

Due to these uncertainties, quantitative methods to evaluate expected risks are inadequate. To cope with the second type of uncertainty, possible development trajectories have been taken into account and described in scenarios.

We defined three scenarios concerning the future development and application of pervasive computing with a time horizon of 10 years:

- **Moderate Scenario:** Pervasive computing will only develop in areas that are already pervaded by networked programmable microprocessors (such as the car).

- Average Scenario: Fields of application and their markets develop according to the trends that can be observed today without being significantly pushed or counteracted.
- High-Tech Scenario: Computing will be highly ubiquitous (anywhere, anytime computing).

These scenarios differ mainly in the degree of diffusion of pervasive computing applications and in the extent of connectivity. Experience gained from ICT products and services show that user behavior has a great influence on the development and diffusion of a technology and that the uncertainties of user behavior are usually high [17]. In the case of pervasive computing, the knowledge of future usage patterns is extremely scarce, as the applications are just emerging.

The application areas chosen for the study were “housing,” “traffic,” “work,” and “health,” of which the most dynamic area was traffic. In addition, three cross-sectional technologies – future digital media, wearables, and smart labels – were investigated, of which smart labels are expected to become the first type of application to form part of our daily life.

All defined scenarios and application areas were reviewed by an external interdisciplinary expert board set up by the client of the study (TA-Swiss). Based on the scenarios defined, we made rough quantitative assessments (case studies) for selected application areas in order to extrapolate trends into the future. Thirty-nine researchers and other experts from industry, NGOs, and public authorities were interviewed after being briefed about the preliminary findings. They were first asked to help identify potential applications of pervasive computing likely to be in place by 2012. Second, their appraisals of the consequences of technological devel-

opments on selected environmental topics were gathered in formal expert interview situations or discussed in expert workshops. Repeated consultations of the selected experts both from science and from politics contributed to the validation of the results and to the identification of priority areas for precautionary action.

The outcome of these expert consultations was a list of the potential opportunities and risks of pervasive computing. After an initial screening the risks were filtered in order to separate the wheat from the chaff and – as a beneficial side-effect of the filtering – to recognize which risks have similar characteristics and can be clustered for complexity reduction [18].

Environmental Impacts

Ecological sustainability will be influenced by pervasive computing in two ways: Pervasive computing will bring about additional loads on as well as benefits to the environment [19]. The environmental impact over the life cycle of single microelectronic components is not expected to change significantly with the trend to pervasive computing.

Resource Consumption

Intel expects that semiconductor technology will develop continuously towards a design geometry of 22 nanometers within the coming ten years without a general change in material composition [20]. Semiconductor miniaturization is accompanying the decrease in the power demand of single transistors, but this savings is being counteracted by a higher structural density and higher power leakage caused by quantum effects [2]. As the acceptable energy consumption of silicon chips (e.g., microcontrollers) for mobile devices is limited by the power supply of batteries or other portable energy sources, the power demand of a single device cannot increase very much.

Assuming that a pervasive computing device has a mobile phone like size and a similar battery capacity of 1500 mAh, it has to be significantly more energy efficient than a 2004 mainstream PC using about 210 W [21] and likewise a Pentium 4 Notebook (23 - 54 W) [22]. The functional requirement of low energy consumption provides a great opportunity to the environment on the macro level as pervasive computing could increasingly replace the PC for many applications (e.g., Internet access).

However, due to the increasing number of components that will be used, the total material and energy consumption caused by the production of electronic goods [23] is still expected to accelerate global resource depletion. The vision of pervasive computing implies that a large number of electronic components will be used in parallel (IBM estimates 1000 components per person [24]). Furthermore, the trend toward throwaway electronics caused by price reductions will shorten the average service life of electronic devices and components in general. For these reasons, a reduction of the total demand for raw materials by the ICT sector can be anticipated only in the moderate scenario.

As the number of components produced will increase dramatically in the Average and the High-Tech scenarios, a compensation or even over-compensation of the material efficiency gains brought about by continued miniaturization and integration is more likely. Fig. 1. shows the dependency between the weight per mobile phone and the total mass of all mobile phones sold in Switzerland as an example. Even though the miniaturization has led to a decrease in physical mass per device, the physical mass of all mobile phones sold has increased sharply [16]. Furthermore, no significant substitution effect occurred because the number of conventional telephones decreased only slightly.

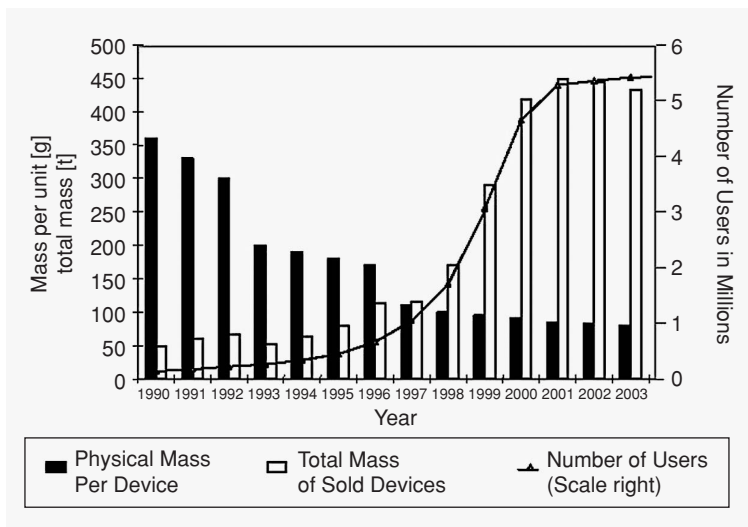


Fig. 1. Individual and cumulative weight of mobile phones sold on Swiss market [16].

Thus the overall resource demand for the production of ICT is expected to grow if the vision of pervasive computing becomes real.

Direct (primary) environmental effects of pervasive computing also occur during the use phase of electronics, mainly due to the energy consumption of network infrastructures, which is estimated to increase as a result of increasing data traffic generated by pervasive computing. Networked household appliances with embedded ICT, which draw energy from the mains, require additional power. The energy demand of the network infrastructure needed for pervasive computing might be as large as several percent of total power consumption [25]. Always-on devices in particular and devices in stand-by mode will also form a substantial part of the total electricity consumption. The energy efficiency of mains adaptors and power management technology will be important factors in the future as always-on devices become more ubiquitous.

On the other hand, there is a great potential for power savings due to the trend to mobile devices because the acceptable weight of mobile devices limits battery size. Energetically more efficient power supply technologies such as

low temperature fuel cells are expected to enter the market in the coming years. In addition the optimization of energy management in buildings and facilities by using more intelligent controls represents a great potential for energy efficiency. However, the risk of additional energy consumption in total may predominate if incentives for rational energy use are missing [26].

End-of-Life Treatment

Another environmental risk of pervasive computing is the release of pollutants caused by the disposal of the resulting waste. Service life is an essential parameter of the waste generation by ICT products: halving service life means doubling the resource use for production and doubling the amount of waste disposed per service unit. As ICT products are often scrapped after a service life of only 10-50% of their technically possible lifetime, there is some risk that this problem will be extended to non-ICT goods with embedded ICT components ("smart objects"). By this effect, pervasive computing could indirectly contribute to an increasing demand for raw materials and an increasing amount of waste. The increasing quantities and shorter service lives

of components that accompany pervasive computing will most probably counterbalance or even outweigh the benefits obtained from progressing miniaturization.

End-of-life treatment of pervasive computing will have to deal with large numbers of small electronic components that are embedded in other products. More and more microelectronic throwaway products, including rechargeable batteries, will be found in waste streams outside that of electronic waste (packaging, textiles). The content of hazardous substances might be uncritical in a single pervasive computing component, but in mass application a release of toxic substances into the environment is to be expected when the components are disposed of in an uncontrolled way or enter recycling streams for materials like paper, cardboard or glass. The invisibility of pervasive computing components in many products will make it more difficult for consumers to differentiate between electronics and non-electronics.

Therefore waste separation by the end-consumers will be almost impractical. As a consequence, the risk of uncontrolled disposal of toxic substances as a part of household waste could counteract the goals of the European Waste Electrical and Electronic Equipment (WEEE) directive [27]. If no adequate solution is found for the end-of-life treatment of the electronic waste generated by millions of very small components, precious raw materials will be lost and noxious pollutants emitted to the environment.

Indirect Effects

In contrast to these primary environmental impacts, there are also second-order effects of ICT, i.e., optimizing material and energy intensive processes as well as substituting pure signal processing for such processes. The potential environmental benefits from such effects are considerable and can

even outweigh the negative primary effects if, for instance, the increasing independence of activities from defined locations reduces traffic [28]. In principle, some business travel can be replaced by telecommunication, which could save long-distance flights in particular [29].

However, using these potential environmental benefits requires that there will be enough incentives to manage natural resources more economically. Otherwise, the growth in demand will counterbalance the savings. These so-called rebound effects are likely to occur, as the history of ICT has shown so far [30]. Pervasive computing could even intensify individual traffic as it supports independence from fixed locations and creates incentives for personal or commercial relationships over longer distances [31].

Whether the positive or the negative effects of pervasive computing on the environment will dominate will depend on how effectively energy and waste policies govern the development of infrastructures and applications in the coming years.

turized and embedded microelectronic components interconnected in wireless networks could have an influence on human health due to the additional exposure to non-ionizing radiation.

Unlike most of today's ICT products, pervasive computing components will be carried close to the human body. Additionally, a predominant part of mobile equipment will stay in an activated mode permanently for functional reasons. As a consequence, a great part of the emitted radiation will be absorbed by body tissue, so that even low-emission power can lead to comparably high local exposure over a longer period [32], although not to thermal effects.

It is undisputed that health damage results from thermal effects by high NIR-exposure. It is generally assumed that an intensity above 100 W/kg has a negative impact on body tissue. Such thermal effects should be prevented by having boundary values for the specific absorption rate of 2 W/kg applying to cell phones. New

sleeping subjects [33];

- Those effects depend on signal modulation, i.e., they do not occur when a non-modulated carrier signal is used [34].

The following are still unknown [35]:

- A causal mechanism that could explain the observed biological effects;
- Whether those effects can cause health damage;
- Whether serious long-term effects of NIR exposure occur.

Furthermore, it is still unclear whether the sensitivity to electromagnetic fields that is observed in single cases is based on biological mechanisms or has a purely psychological explanation.

Besides the biological effects, attention must be given to the psychosomatic effects of NIR exposure. Anxious people can develop real symptoms if they are convinced that NIR threatens their health.

Only under far-reaching assumptions might pervasive computing make possible a stabilization of, or decrease in, our daily exposure to non-ionizing radiation. An increase is more probable, as wireless local area networks (W-LANs) are being built in addition to mobile phone networks. In spite of their lower transmission power, they will add to the total exposure, unless they are used as a substitute for existing networks. Considering the fact that pervasive computing involves wearing radiation sources on the body (wearables) and even inside the body (implants), there is a need for further research. Even sources of low transmitting power may cause high exposure to radiation if they are very close to body tissues. There is a conflict potential, as non-users of pervasive computing will see themselves exposed to impairments caused by others, such as in the case of "passive smoking."

One must ask whether these new technologies might have undesirable side effects that counteract the aim of a sustainable information society.

Health-Related Aspects of Pervasive Computing

As we have shown in the preceding sections, impacts of pervasive computing can affect not only natural resources but also human health due to the emission of pollutants. Beyond that, human health will also be affected by technical characteristics of "smart objects" and the way they are used.

Exposure to Non-Ionizing Radiation (NIR)

First, the ubiquitous use of minia-

aspects, however, will arise in the context of pervasive computing, as carrier frequencies differ from application to application.

The current controversy over health risks of cellular phone networks refers to athermal effects of non-ionizing radiation, i.e., effects that occur at low exposure. The following facts are known from past studies:

- Non-thermal biological effects have been observed in electroencephalograms done with

Physical Contact with the Human Body

Physical contact with microelectronics can cause health risks as well. With the growing number of devices worn closely to the human body, a more intensive dermal contact with the surface of these products (polymers with additives) is inevitable. Grit and effluvium can be reabsorbed or inhaled during longer periods. Due to the wide range of substances used for microelectronics the risk of allergic reactions or chronic poisoning increases. Of course, the level of risk depends on the substances used and the kind of encapsulation or other design measures taken to prevent abrasion or effluvium.

optical activity [36];

- Effects of high local NIR exposure in very small areas within body tissue caused by active implants.

Those risks can be influenced by design engineering of implant wrapping and clinical tests. There is need for further research in this field.

Psychological Stress

Further health-relevant effects of pervasive computing can be caused indirectly by influences on user behavior and the social context encountered. In particular, pervasive computing could cause stress for various reasons, such as poor usability, disturbance and distraction, the

of pervasive computing are expected in the form of medical prevention, treatment and health care. In particular the quality of life for chronically ill or convalescent patients can be improved as their dependence on hospital facilities will be reduced by new remote methods of personal health monitoring and by active implants [38]. These medical opportunities will be accompanied by the risk that active implants might have unexpected side effects or that an “over-instrumented” medicine might have negative psychological impacts on patients subjected to close observation.

To sum up: Pervasive computing is expected to cause undesirable effects on human health that could counterbalance the opportunities in the health sector. From the viewpoint of sustainability, this suggests that the risks should be minimized by applying precautionary measures to early stages of technological development.

Social Implications

Social effects of pervasive computing have to be seen as relevant for sustainable development as ICT is expected to interact intensively with social practices, which may result in profound changes to social rules and structures in the near future. Compared to the environmental risks or direct health effects, an assessment of the socioeconomic effects has to deal with even higher uncertainty because societal development trajectories are almost unpredictable.

As social and environmental effects are tightly intertwined (social practices affect the environment, and vice versa), it seems advisable not to restrict the precautionary principle to environmental regulations, but anchor it also in other parts of legislation such as, e.g., consumer regulations. In the following section, sustainability-related opportunities and risks of pervasive computing for society are illustrated.

As the diffusion of pervasive computing is coupled with considerable investments, it will be very difficult to change at a later date.

Active implants, i.e., microelectronic devices inside the human body, provide great therapeutic advantages. They can be used as a component of computer-controlled prostheses such as brain pacemakers or for artificial sense organs.

There are possible side effects of active implants that are still unexplored:

- Health reactions to substances that are dissolved from the implant surface;
- Influence on functionality and behavior of cells that are in direct contact with the implant surface (protein adsorption or denaturation on the implant surface);
- Mechanical stress within the body tissues surrounding the implant;
- Disturbance of cell-cell interaction caused by electrical or

feeling of being under surveillance (privacy issues), the possible misuse of technology for criminal purposes, as well as increased demands on individuals' productivity. Stress has a considerable impact on health.

Although there is a promising opportunity for better adaptation of pervasive computing to human needs [37], experience with established ICT shows that interfaces with poor ergonomic quality are widely accepted by consumers. There is a general trend to frequent distraction of human attention caused by technical devices. Such disturbances are likely to increase in future due to the diffusion of pervasive computing gadgets. The question is still open as to how such harassments can be effectively prevented.

Medical Applications

The most significant opportunities

Digital Divide

New forms of human-computer interaction can lower physical and intellectual barriers against the use of ICT and facilitate participation to the information society. That could contribute to a reduction of the digital divide separating social groups [39]. According to the vision of pervasive computing users will be relieved from technical restrictions by better usability [40]. Handicapped and sick persons will benefit from applications that lower physical barriers, or even permit access for the first time. From the present point of view it is hard to anticipate whether pervasive computing will reach a higher degree of adaptation to the human.

Consumer Freedom of Choice

At the same time there is a risk that consumer freedom about which technology to use will be limited. As ICT dominates more and more activities (such as banking, learning, and shopping), alternatives to using ICT may disappear in certain cases. Persons who do not want or are not able to use ICT for certain reasons would be practically excluded from such services. The consumer can no longer decide freely for which activities he or she will use ICT, or not.

Moreover, a loss in competition among service providers may occur if proprietary de-facto standards continue to play a significant role in the computer economy. As a result the consumer may lose the power to decide which ICT products or ICT services he uses and what price he pays. This is true in the first instance on the software level. But as real world objects become equipped with microcomputers that make them smart, the problem may extend also to physical everyday objects. Smart objects will usually operate only with proprietary software and when the user holds the rights for using it. Technologies for digital rights management may be

applied not only for digital content but also for the regulation of the functionality of physical objects. Auto-identification of objects by Radio Frequency Identification (RFID) may be utilized in this way for instance. Producers may use their enhanced influence on the functionality of “smart” objects for the purpose of market separation or forcing customer loyalty. Due to the loss of market forces in a monopolized market situation, the quality of ICT products and services may decrease.

Information Overload

Access to information and knowledge will work more efficiently under pervasive computing. Access will be possible everywhere and anytime (pervasiveness), and be dependent upon one’s location and local environment (context sensitivity). That will provide better opportunities for self-determined forms of learning and participation. Users will get easy access to context-based information (city map, timetable), enabling better orientation and decision-making. Furthermore, new forms of work (mobile teleworking) will be supported.

On the other hand, the user will be flooded with information even more than by the Internet today. In contrast to present ICT, pervasive computing will surround humans almost all the time (in particular if implemented as “wearable computing”) and makes them a target for uninterrupted influence. In a market situation the user’s conscious attention will become a scarce resource that will be hard fought after by commercial advertisers. It will become more and more difficult to relax mentally by merely turning away from the information flood. Even if the user has the ability to refuse the perception of information, in every case he has to make a decision how to handle a message or stimulus first.

There is a risk of general increase of disturbance and interruption caused by pervasive computing.

Privacy

Pervasive technologies also pose a problem for privacy. In particular, RFID transponders in the form of “smart labels” will probably become the first and most widespread example of pervasive computing. With “smart labels” it will be much easier to protect goods from theft or imitation. In combination with new computer-supported authentication technologies (e.g., biometrics) there is an opportunity to protect buildings and facilities better from unauthorized access.

The same technology can also be used to intrude on the privacy of people. As RFID is intended to be used for unique identification of real-world objects (e.g., items sold in supermarkets), RFID systems can also be used for tracking the owner of the item as well as for object monitoring after the point of sale [41]. Accumulation of RFID transaction data by the regular RFID owner can threaten privacy more than eavesdropping of unsecured radio-frequency interfaces by a third party. Such data collection is already under discussion with the payback cards offered to customers by retailers. “Smart labels” will aggravate this problem, as a more extensive collection of personalized data is possible and the consumer is nearly unable to control access to the data. This applies to both the data stored on the RFID chips and those in centralized databases and associated with the unique IDs of the chips. Both data privacy and location privacy will be difficult to ensure in a world pervaded by RFID transponders.

Security Issues

New forms of computer crime may emerge due to the refined networking, embedding, and pervasiveness

of ICT. Security can be undermined by the susceptibility to failure of a complex technology that is not well designed. Security vulnerability of software that promotes criminal abuse has already become a major economic problem in the use of Internet. In the case of pervasive computing, ICTs not only process information, but can also control physical processes. As a consequence, failure or criminal attacks in the virtual sphere can threaten physical inviolacy of persons and infrastructure.

the study? In environmental policy, the precautionary principle has been established as an appropriate concept for action in face of technology caused risks to the nature and human health. In the context of sustainable development, an expansion of precautionary principle for social implications should be discussed.

To make pervasive computing sustainable, precautionary measures have to be initiated at the earliest possible time. First of all, national strategies towards sus-

cautionary principle motivates developers and users to explore technological alternatives [44].

Foresight and monitoring activities are appropriate methods to generate early warnings about risks caused by novel technologies. Scientific minority opinions may serve as early warnings [45]. The German Advisory Council on Global Change (WBGU) even promotes broad participation by civil society in the discussion of new technologies in order to create knowledge about uncertain risks and deal with them adequately [46]. The Swiss Centre for Technology Assessment (TA-SWISS) organizes citizens' participation panels ("Public-Forum"), which is a promising approach in this context [47].

Pervasive computing becomes interesting for business and consumers when opportunities outweigh the risks – not only for the individual user, but also for society and the environment. The principle of sustainability is therefore an appropriate concept in order to achieve acceptability for this evolving technology.

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Early precautionary measures could avert irreversible negative developments cost effectively.

Opportunities and Risks

Within the context of the scenarios of the study [16] we have identified areas in which pervasive computing may both collide with or support the aim of a sustainable information society.

It is difficult to forecast the relative dominance of opportunities or risks due to the openness of the development, i.e. the highly dynamic character in technological and social development processes. It is to be expected that pervasive computing will amplify already existing trends such as growing e-waste streams, increasing power consumption for digital networks or increasing exposure to non-ionizing radiation. Pervasive computing can also generate stress for various reasons, such as poor usability, disturbance and distraction, the feeling of being under surveillance (privacy issues) or the well-founded fear of misuse of the technology for criminal purposes. Therefore pervasive computing has the potential to influence social practices and to lead to social conflicts.

What would be an adequate reaction to the risks identified by

sustainability, such as the Swiss Sustainable Development Strategy [42] are to be seen as a paradigm for the technological innovation process. Without a clear strategy to promote the social and ecological compatibility of new technologies, the innovation process would be purely technology-driven and might cause severe conflicts and high external costs in the future.

Application of the precautionary principle would also require more product stewardship from leading ICT producers. It is essential that development trajectories be adjusted towards sustainability before pervasive computing components become mass products because of the socioeconomic irreversibility of technology diffusion: after launch it will be difficult to correct any resulting disadvantageous effects. Adequate measures to manage product stewardship in the field of environmental aspects are eco-design, the inclusion of Life Cycle Assessment (LCA) in the decision process, and Life Cycle Costing (LCC) [43]. Furthermore the pre-

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Energy-aware software design: A hardware and behavioural consumption scoring and labelling algorithm

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ABSTRACT

Energy labelling of electric appliances, among other goods and services, matters to both consumers and producers. It aids the consumers in gathering an expectation of how much electricity the good they are acquiring will consume, and it establishes guidelines that producers can follow to create more efficient products and services. Energy labelling is, nevertheless, nearly absent for software due to the heterogeneous hardware execution environments and unequal circumstances. Throughout this article, we provide an equal ground for software energy labelling by providing an intelligent prospective behavioural, hardware and energy-consumption patterns rating algorithm meant for the design phase of the Software-Development Life Cycle, as opposed to the existing approaches whose labelling procedure applies to the implementation phase onward. We expect our categorization method to guide software developers, helping them to decrease their energy consumption by raising their awareness of the energy footprint that their design choices produce; culminating in aware consumers who know what energy usage to expect from the software they consume.

1. Introduction

Economic growth has become an unreliable metric to assess societies' progress (Comission, 2021b), as its reliance on limited natural resources to create economic value through services and products directly influences our environment. To reduce the impact of our consumption, the global consensus is to dial down its rate and amount. Contrary to the previous goal statement, electrical devices are manufactured and introduced into our global network each year.

To address the electrical energy consumption of such devices, several design, manufacturing, and testing guidelines have been garnered within the European Union to provide an equal ground for manufacturers. One example of these guidelines is the MEERP (Methodology for Ecodesign of Energy-related Products), issued by the European Commission. The MEERP's objectives are twofold: (1) creating a common ground for the ecodesign of energy-related products (any good that has an impact on energy consumption during use) to harmonize national laws of the member states of the European Union (2) and contribute to sustainable development (Comission, 2017). The content of the MEERP establishes a framework for the ecodesign of energy-related products to ensure their free movement within the EU's internal market, and a set of

requirements that such products must fulfill to be put on the market or into service. The initial version of the MEERP, issued in 2011, was last revised in 2013 to include in it new variables such as material efficiency.

In addition to the design and manufacturing guidelines, labels that categorize the electrical consumption of energy-related products have emerged to aid consumers in selecting a more frugal device. The most prominent example of an energy consumption indicator is the European energy label. The EU energy label was first introduced in 1994 and expanded in 2004 (Comission, 2024a), with a significant re-scale in 2021 to push forward energy savings and update the standards of the label (Comission, 2021c). It consists of a visual categorization of the electrical consumption of a product in a color scale and letters, from A in green to indicate the best efficiency to G in red to indicate the least efficiency.

Furthermore, the label includes the annual energy consumption of the device and iconography that corresponds to key traits of the product that should matter to the consumer (e.g., the noise level under operation). Its simplicity allows consumers to quickly identify the most frugal device in the category of their interest, saving money for the consumer with the most frugal device that satisfies their needs, and achieving the entrance of the least wasteful product

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to households. The EU energy label is a proven success, as the special barometer 492 discovered that nearly 8 in 10 of the people surveyed said they have been influenced by the EU energy label when choosing a product (Kantar, 2019).

In the EU, computers (desktops, workstations, notebooks, and tablets among others) are also considered by the ecodesign directive and requirements, as defined in the commission regulations (EU) 2021/341 and 2013/617 (Comission, 2013, 2021a) but they are currently absent of a label. The public consultation phase for labelling computers, according to the initiative “Energy labelling requirements for computers and computers servers” (Ares 2018 770774) has recently closed, and its EU commission adoption is planned for the fourth quarter of 2025 (Comission, 2024c). The feedback that the initiative has received so far has heavily criticized the fact that a comprehensive duty cycle and power study is necessary to define a significant usage, emphasizing that proposing an energy label based on an active power metric is premature (Comission, 2024b). Similar initiatives are in development in other parts of the world, for example, the Energy Star program in the EE. UU. Star (2024), which is included in the feedback of the EU’s initiative as a possible starting point.

As software rules the consumption of hardware by providing it instructions to operate on, creating a common ground for a measurement that relies heavily on the purpose of the software consumed is after all a complicated task. However, to our knowledge, no ecodesign directives on software in the EU, as the definition of an energy-related product does not cover the intangibility of software. This lack of ecodesign requirements acts as a detriment to the consumers of software, as no widespread energy label similar to EU’s is applied to the consumption of software to guide them in the same way they are guided when acquiring home appliances. As observed by Guamán et al. (2023) “it is necessary to construct energy estimation models that do not require the execution of applications.”.

Throughout this article, we will present the previous and current efforts at categorizing the energy consumption of software and software designs, and present our categorization algorithm aimed at the design phase of the Software Development Life-Cycle (SDLC). In particular, the contributions of the proposed work can be summarized as follows:

- A series of qualitative properties and values that describe the behaviour of software at an architectural and behavioural level.
- A series of quantitative properties that describe the magnitude of hardware consumption inflicted on a hypothetical hardware platform using a software design.
- An expert algorithm to estimate and categorize the hypothetical hardware resource consumption and behaviour of software, based on the aforementioned properties.
- A version of the expert algorithm to estimate and categorize the hypothetical hardware resource consumption of the parallel execution of hypothetical software operations.
- A series of weights that prioritize certain properties and their combinations over others, as well as the impact they represent when obtaining a categorization label for the total consumption obtained from the software unit’s design.
- Thorough experimentation and comparative analysis between software-mandated consumption of hardware resources and how the differences in hardware capacities affect the results.
- Guaranteed transparency and replicability of our experiments with publicly available information and data on the setup, execution, results, and project files including a public beta of our custom tool to run them in 5 simple steps.

In Section 2, we delve into the existing approaches and propositions for categorizing or labelling software’s hardware consumption. In Section 3 we explain the previous research artifacts that support the expert algorithm proposed in Section 4. In Section 5, we present the experiments that test our algorithm proposal under diverse circumstances, explain the methods employed for their execution, and their results. The

closure of this paper is performed in Section 6 discussing the results in the previous section, and closing our article with our future work.

2. Related work

The idea of categorizing software with labels that reflect its consumption of energy or hardware resources is not new. In the work of Deneckère and Rubio (2020), software criteria are classified according to the three phases of the SDLC, which the Greensoft model also follows: development, usage, and end-of-life (Naumann et al., 2011). In contrast to Greensoft, they concentrate on two software aspects: sustainability by software and sustainability in the software (Calero & Piattini, 2015). Even though the criteria they proposed provide a good starting point for understanding the elements to consider in labelling a software product, they did not propose a concrete form of categorization. Moreover, to our knowledge, the authors did not pursue any pragmatic implementation of their proposal. By contrast, the GreenSoft Model Methodology (GSMM) has been further developed into a practical testing framework that is now applied in much of the green coding community (Naumann et al., 2021). The GSMM defines measurement points for energy and resource consumption across the software life cycle, with a strong focus on runtime efficiency during the usage phase. In practice, this involves standardized benchmark environments, monitoring of CPU, memory, storage, and network demand, as well as the evaluation of scaling behaviour under different workloads (Guldner et al., 2024). These methodological foundations directly inform ecolabel schemes such as the Blue Angel, which adopts comparable measurement proposals for mobile, desktop, and server platforms.

The “Blue Angel for Software Development and Components” (Angel, 2023), commissioned by the German Federal Environment Agency, is the German equivalent of the EU’s energy label. Blue Angels have existed for more than 45 years without any mandatory aspect, as they are completely voluntary and independent of business interests. There are currently more than 12,000 products and services that bear the Blue Angel, representing the best in their respective product group. As explained by Naumann et al. (2021), up to 2024 the scope of the award criteria was limited to software that has a user interface and can be run on a benchmark desktop system. In the same year, the organization updated its standard to also include mobile applications, desktop software, and server–client platforms. In the revised Version 4 of the criteria (June 2024), the Blue Angel additionally introduced concrete measurement proposals that are applicable across these device classes, thereby extending the methodology beyond its earlier desktop-only focus. The main components of the award criteria are grouped as follows:

- **Resource and energy efficiency:** definition of minimum system requirements, transparency of energy and resource demand, support for operating system energy-saving modes, avoidance of unnecessary background processes, and the use of efficient algorithms and data structures. The software must not place disproportionate demands on CPU, RAM, storage, or network resources relative to its functionality.
- **Potential hardware lifetime:** compatibility with older hardware and operating systems, adaptability to upgraded systems, and avoidance of artificial obsolescence. The software must maintain efficiency even as functionality expands, and vendors must commit to update policies that extend rather than curtail the useful life of hardware.
- **Autonomy of use:** provision of standardised, interoperable, and openly documented data formats, guaranteed uninstallability without residue, ability to run without permanent internet connectivity, and absence of advertising, forced cloud connections, or hidden costs. The criteria also emphasize user control over the configuration, update installation, and data portability.
- **Transparency and user rights:** clear and publicly available documentation of functionalities, system requirements, data collection,

and update cycles. Vendors must disclose whether telemetry or analytics are collected, avoid vendor lock-in strategies, and respect user privacy by default.

- **Quality assurance and robustness:** the criteria require that software undergo systematic quality testing, maintain backward compatibility where possible, and demonstrate resilience against faulty input or unexpected system conditions. These aspects ensure that efficiency and usability are not only theoretical but also practically verifiable.

Concerning green-oriented tools, whose specific mission statement is to aid in the creation of green software, several have their own method of labelling the hardware consumption, energy consumption, or greenhouse emissions. For instance, Wilke et al. (2012) intended to establish a framework that allows profiling the power consumption of mobile applications so that a long-run approximation of their energy consumption can be generated. In addition, they considered that usage profiles should be used together with the profiled power consumption. The authors proceeded to describe a case study of two different email clients for Android and discussed the results and the variables that impacted their study. In the end, no actual energy label was proposed. Even though the authors remain active in the topic of sustainable software, further research built on this proposal appears to be halted, and the specific algorithm employed to generate a label was not disclosed.

Carat (Oliner et al., 2013) constructs energy models of Android applications using a crowd-sourced approach, and later assigns a J-score to the results of the learned energy consumption patterns of the device running it. The J-score is a percentile, meaning that a score of 75 would mean that the device has a better battery life than 3 in 4 devices.

ecoCode (Le Goaer & Hertout, 2022) provides multiple plugins for SonarQube, a code evaluation platform. EcoCode currently supports Java, JavaScript, PHP, Python and C#. It also includes plugins for Android and iOS, which use a set of *smells* to detect “bad” (energy-consuming) code patterns. The labelling mechanism employed in EcoCode is the green quality gate, a SonarQube quality gate with a threshold of four issues, meaning a project is not publishable if four or more issues are present.

EcoDroid (Behrouz et al., 2015) employs static and dynamic analysis of applications in the same category to obtain their energy consumption. It is capable of generating an alphabetic label from A (most efficient) to E (least efficient) from an e_{index} obtained by dividing the energy expenditure of API calls per path possible in an application’s call graph by the total number of visited nodes out of all the nodes available in the call graph.

EcoIndex (Bordage, 2024) uses the information on the user’s hardware, the data transferred over the network, and the number of server requests to create a score of a website. These three dimensions are used to obtain a weighted average that concludes in an alphabetic label from A (most frugal) to G. EcoGrader (Mightybytes, 2024) uses CO2.js to obtain available metrics of energy consumption in data transfer and content load time, among others, to rate websites. EcoGrader has two different labels: the EcoGrader score based on the Sustainable Web Design Model (Design, 2024), and the Digital Carbon Rating based on HTTP Archive Transfer Size (kb) and grams of CO2 emitted per pageview. Globemallow (2024) is similar to EcoGrader and EcoIndex, but it uses the American grading system to rate webpages from A+ (most efficient) to F.

Kastor (Spécinov, 2024) is another tool similar to EcoIndex, EcoGrader and Globemallow. Kastor gives scores for accessibility, ecodesign, and also a global score, all based on a weighted average according to a study of good practices. A leaderboard of webpages based on the global score is also available. The Experiment Impact Tracker framework (Henderson et al., 2022) proposes (but is not limited to) carbon-friendly cloud-provider regions to be used for Machine Learning (ML) training, and a proposal of a leaderboard of frugal ML approaches as an incentive for more frugal ML. The leaderboard is based on the

performance return per kWh consumed by the ML model. Li10 (2024) reports the estimated CO₂ emissions of hardware consumption on different cloud computing providers. Li10 assigns a Cloud Sustainability Score to the user’s emissions, its precise algorithm is, to our knowledge, proprietary.

While existing labels and tools like Carat, EcoCode, and EcoGrader provide valuable insights into software efficiency, they all rely on analyzing a functioning application. In difference to other planned energy-saving strategies that can be created before software deployment, such as scheduling algorithms (Fernández-Montes et al., 2012; Sun et al., 2023), reactive software categorization approaches miss the opportunity to incorporate sustainability into the software design from the very beginning. Recent community studies, such as Potentials of Green Coding (Junger et al., 2024), highlight this same limitation: most tools operate at runtime, monitoring CPU, memory, storage, or network consumption under specific workloads, and then translating these observations into scores or labels. While this provides valuable transparency for deployed systems, it does not directly guide architectural or design choices. A proactive approach would empower developers to make informed decisions early on, ultimately leading to greener software and a more sustainable future.

Recent community studies, such as Potentials of Green Coding (Junger et al., 2024), highlight this same limitation: most tools operate at runtime, monitoring CPU, memory, storage, or network consumption under specific workloads, and then translating these observations into scores or labels. While this provides valuable transparency for deployed systems, it does not directly guide architectural or design choices. As noted by Green Software: The Curse of Methodology (Hindle, 2016), precise measurement is inherently limited by the constant evolution of software and hardware, and progress requires bridging multiple disciplines toward a common goal. **Our goal is therefore to create a software sustainability label applicable during the design phase, even before any implementation effort, as opposed to existing approaches that require functional code to work and fine-grained metrics that can quickly become obsolete.** A proactive, high-level framework empowers developers to make informed choices early on, leading to greener software and a more sustainable future.

3. Research artifacts employed

Before introducing our proposal for the algorithm, we believe there are several important research artifacts that need to be briefly introduced to provide a better picture of its role within a custom toolset for green software design we created. The first artifact is the Behavior-Based Consumption Profiles (BBCP) (Larracoechea et al., 2024a), a Domain Specific Language (DSL). The objective of the BBCP is to model the behaviour of software and the evolution of its hardware consumption over time. In contrast to other approaches, the BBCP allows software designers to embed an estimation of the intensity of the usage of the software in its design, meaning that the resulting software design considers dynamic variations that cannot be controlled but should be accounted for, such as volatile user behaviour and stochastic events.

Later, the aforementioned intensity can be translated to an estimation of hardware resource usage and, therefore, energy consumption. The BBCP consists of properties at 2 levels of abstraction: the high level and the low level. The high level is constituted by qualitative properties that describe the non-functional behaviour of the software. The low level consists of quantitative properties that assert how the behaviour of software is translated into hardware consumption.

The algorithm explained throughout this paper employs 3 sets of high-level properties and 1 set of low-level properties of BBCP. The high-level properties are distributed into 3 categories, which are available in Table A.1 of Appendix A. The categories, which are similar to the ones proposed by Forward and Lethbridge (2008), are used to generalize each group of properties. Properties in the *computation-centric* category are responsible for the description of how the software product is expected

Table 1

The qualitative properties included to provide data to the algorithm on the consumption of hardware sensors.

Sensor	Possible values
NIC	On/Off/Circumstantial/Absent
Bluetooth	On/Off/Circumstantial/Absent
GPS	On/Off/Circumstantial/Absent
Camera	On/Off/Circumstantial/Absent
Display (brightness level)	Off/Low/Medium/High/Auto/Absent

Table 2

Quantitative properties fed to the algorithm to account for hardware resources and the programming language used to execute the demands of software.

Resource	Unit
CPU	GHz
RAM	GB
Network (NIC)	Mbps
Storage	GB
Programming Language	Rank in Pereira et al. (2021) re-ranked version of rosetta code

to perform its computation. *Data-centric* properties describe the relationship of the software with data it creates or receives. Finally, the *conduct-centric* category contains properties that describe the behavioural traits of the software. A more in-depth explanation of each property is available in previous related publications ([Larracoechea et al., 2021, 2022, 2024a](#)).

In contrast to previous approaches, the BBCP categories reflect a coalition of three perspectives: the Behavioural-Software Engineering (BSE), which describes how the business decisions affect the software's design; Human-Computer Interaction (HCI), which describes how the user interacts with software and vice-versa; and the Software Architecture (SA). For instance, the properties in the Computation-Centric category reflect the needs of the business and the software architecture. For example: a computation criticality value of *high* can reflect the necessity to comply with a pre-defined quality-of-service response time stipulated in service-level agreements (SLAs), while a computational complexity with a *high* value can reflect that the software architecture must be tailored to prioritize response time (the computation criticality), at the cost of a greater computational expense.

In addition, the properties in the behaviour-centric category describe behavioural characteristics that could be attributed to the user. For example, a *definite* consumption rate could be used to describe a video streaming service, as the activity's duration is equal to the content's length. For example, a 1 h-long movie has a definite consumption rate of 1 h. The importance of the properties selected for the algorithm is that they allow us to better understand the intent of the software designer, and take the qualitative characteristics of the software's design into account as a possible source of power expenditure.

To complement the aforementioned properties, we include some sensor-related properties, which are available in [Table 1](#). The purpose of these properties is to define the state of a specific sensor at the time of software activation, participating in our method as a dependent variable that could significantly impact the final categorization of a software design. As for the low-level properties, we are limiting the algorithm to five of them, which are available in [Table 2](#). They represent the basic hardware resource consumption that software usage requires in any computing hardware platform, and they are the most common metric to specify hardware requirements that software developers propose for the execution of their software products.

The last elements that are directly related to the low-level properties and play a significant role in making our algorithm useful during the design stage of software development are *consumption guides*. Consumption guides are databases meant to guide software designers through

the process of selecting an approximate amount of hardware consumption per available hardware resource. They are a by-product of the process we followed during the development of the BBCP, in which we performed a taxonomical study of software-induced hardware consumption. Throughout the process, we collected multiple samples of software-hardware requirements that were made publicly available by their software developers, based on several horizontal slices of software types and categories we selected from the taxonomy created by [Forward and Lethbridge \(2008\)](#).

The second research artifact involved in the creation of our algorithm is RADIANCE+, formerly known as "RADIANCE" ([Larracoechea et al., 2024b](#)). RADIANCE+ is a custom-built tool designed with three main objectives in mind: (1) assist in the creation of BBCPs, (2) interpret the BBCPs with a custom execution engine, and (3) categorize and label software designs based on the BBCP DSL according to user-customizable algorithms. The proposal in this paper addresses the third objective.

A multistep process incorporated in RADIANCE+'s workflow begins by providing the users with a selection of choices per four core properties: software category, software type, target hardware platform (desktop, mobile, and laptop), and programming language, all at a per-operation level and within the context of a BBCP profile. Additionally, consumption guides pre-built consumption guides are incorporated into RADIANCE+ to improve the convenience of the design process. For the programming language, the user is presented with a list of options to choose from, which originates from a study performed by [Pereira et al. \(2021\)](#) on the energy consumption of programming languages. It is important to note that subsequent research has questioned the conclusions of the previous study. Thus, [van Kempen et al. \(2025\)](#) argue that, when controlling for factors such as the programming language implementation, application implementation, number of active cores, and memory activity, the choice of programming language has no significant impact on energy consumption beyond the execution time. This suggests that the energy efficiency rankings proposed by Pereira et al. may not be as definitive as previously thought. In light of this, RADIANCE+ offers a flexible framework where users can update and refine the consumption guides as new research emerges with better consumption guides. This adaptability ensures that the tool remains relevant and accurate, allowing the community to contribute to the continuous improvement of energy consumption models.

4. The expert algorithm

Our algorithm consists of 3 core parts: the Hardware Consumption Score (HCS), the Sensor Consumption Score (SCS), and the Behavioural Consumption Score (BCS). This compartmentalization was selected as a representation of our aforementioned interest in the BSE, HCI, and SA perspectives, explained in [Section 3](#), so that each component can be characterized and studied independently from each other.

The HCS is a metric that quantifies the consumption of various hardware resources on a computing system. More specifically, the HCS is a weighted usage ratio of different hardware resources, which include the CPU, RAM, network, and storage devices. It uses the properties in [Table 2](#) and considers a hypothetical resource availability to approximate the usage ratios per hardware resource:

- CPU efficiency ratio ($\frac{TDP}{C_a} \cdot C_c$): This is calculated by dividing the CPU's Thermal Design Power (TDP, in watts) by the CPU base frequency reported by the hardware manufacturer (C_a , in GHz) to obtain the power-to-cycle ratio, which is later multiplied by the hypothetical consumption of the CPU (C_c) to obtain an approximation of the power consumption and heat output to dissipate. Moreover, this method punishes raw CPU cycle capacity and fosters efficiency. While this method of deriving a metric for CPU consumption may not offer absolute precision, it invites a broader discussion on how alternative approaches risk conflating the characterization of software behaviour with that of hardware performance.

Table 3
The weight of each hardware resource.

Tag	Component	Weight
$NETw$	Network Interface Card (NIC)	0.5
$CPUw$	CPU	0.3
$RAMw$	RAM	0.1
STw	Storage	0.1

- RAM usage ratio ($\frac{Rc}{Ra}$): Similarly, this is calculated by dividing the RAM consumption (in GB) by the RAM availability (also in GB).
- Network Interface Card (NIC) usage ratio ($\frac{Nc}{Na}$): This is calculated by dividing the network card consumption (in Mbps) by the network card availability (also in Mbps). The consumption is calculated by obtaining the average between the transmission and reception.
- Storage usage ratio ($\frac{Sc}{Sa}$): Finally, this is calculated by dividing the storage consumption (in GB) by the storage availability.

As previous research has found, the programming language plays an important role in the final power consumption of software. In addition to the previous ratios, we employed the global Rosetta Code ranking of programming languages (Code, 2025), which was re-ranked according to energy consumption by Pereira et al. (2021). To do so, the position in the aforementioned rank of the selected language is multiplied by a weight. The weight value employed was obtained by calculating the Inter-Quartile Range (IQR) of the rankings and dividing the result by 1000 to tone down the relevance of the result in the HCS. We are aware that during the design phase the values of the aforementioned properties are difficult to predict to produce accurate ratios. Nevertheless, we propose that software designers and architects use the system requirements of a similar application to the one being built as a frame of reference. Moreover, a market analysis of the target users and target hardware platform should be included in the process of obtaining the ratios to facilitate the decision-making.

In addition to the ratios, a system of weights whose individual values range from 0 to 1 is included to prioritize the consumption of certain hardware components over others. As seen in Table 3, we distributed the weights so that the network card carries the heaviest weight, with the CPU following it in second place and the RAM and storage in the last place. This hierarchy is backed by the results of previous research (build-computers.net, 2023; Carroll et al., 2010; Chiaravallotti et al., 2011) where the consumption of hardware components is assessed under diverse circumstances to analyze the portion of the battery that each one consumes. The final HCS is given by Eq. (1).

$$HCS = \left(\sqrt{\frac{TDP}{Ca} \cdot Cc} \right) \cdot CPUw + \frac{Rc}{Ra} \cdot RAMw + \frac{Nc}{Na} \cdot NETw + \frac{Sc}{Sa} \cdot STw + Pr \cdot Pw \quad (1)$$

where:

- TDP = the TDP value of the CPU model of the target hardware
- Cc = a deliberate CPU consumption value from a BBCP
- Ca = a deliberate CPU availability value
- $CPUw$ = the weight value for the CPU in Table 3
- Rc = a deliberate RAM consumption value from a BBCP
- Ra = a deliberate RAM availability value
- $RAMw$ = the weight value for the RAM in Table 3
- Nc = a deliberate NIC from a BBCP
- Na = a deliberate NIC availability value
- $NETw$ = the weight value for the NIC in Table 3
- Sc = a deliberate Storage consumption value from a BBCP
- Sa = a deliberate Storage availability value
- STw = the weight value for the Storage in Table 3
- Pr = the rank of the programming language in Pereira et al. (2021)
- Pw the IQR of the rankings divided by 1000

Table 4
Available sensors taken into account in the Sensor Consumption Score.

Sensor	Weight
Network Interface Card (NIC)	0.25
Bluetooth	0.25
GPS	0.2
Camera	0.1
Display brightness	0.2

Table 5
State-dependent values per sensor.

Sensor	State	Value (0 to 1)
NIC	Absent	Redistribute weights and remove from the HCS
NIC	Off	0
NIC	On	1
NIC	Circumstantial	0.5
Bluetooth	Absent	Redistribute eights
Bluetooth	Off	0
Bluetooth	On	1
Bluetooth	Circumstantial	0.5
GPS	Absent	Redistribute weights
GPS	Off	0
GPS	On	1
GPS	Circumstantial	0.5
Camera	Absent	Redistribute weights
Camera	Off	0
Camera	On	1
Camera	Circumstantial	0.5
GSM	Absent	Redistribute weights
GSM	Off	0
GSM	On	1
GSM	Circumstantial	0.5

Table 6
Possible values for the display's brightness level.

Display brightness (Db)	Value (0 to 1)
Absent	Redistribute weights
Off	0
Low	0.4
Medium	0.6
High	1
Automatic brightness control using an external mechanism or sensor	0.5

The next part of our algorithm, the Sensor Consumption Score, considers the array of sensors in Table 1, which are most frequently present in smartphones. The only purpose-specific sensors we decided to include are the GPS and the camera, due to the relevant energy consumption of the GPS and the relevant data generation of the camera. A similar weight system to that in the HCS is also included, with the Wifi/4G (NIC) and Bluetooth considered the "heaviest", followed by the GPS and, finally, the camera. The weights are available in Table 4, and we distributed them to reflect the results of the research performed by Khan et al. (2016). There are four possible states per sensor: *Absent*, *off*, *on*, and *circumstantial*, where a *circumstantial* value attaches the sensor state to the software state (the sensor value applies as long as the software is directly in use), and an *absent* sensor means that the weights should be modified to account for a lack of a specific sensor by distributing its weight evenly across the available sensors. Not distributing the weight of an absent sensor would lead to skewed results, where the profiles whose target platform lacks a sensor would receive an unfair advantage. A quantitative value for each sensor state is available in Table 5.

The last addition to the SCS is the display brightness (Db), whose state-dependent value is available in Table 6. We decided to include the display brightness in the SCS as the research performed by Chen et al. (2013) reveals an important consumption of electrical energy generated by the display, especially when paired with the camera. The total SCS

is given by Eq. (2), using the values in Tables 5 and 6 as an input for the variables, and the values in Table 4 for the weights.

$$SCS = Nv \cdot Nw + Bv \cdot Bw + Gv \cdot Gw + Cv \cdot Cw + Db \cdot Dw \quad (2)$$

where:

- Nv = a value used for the NIC in Table 5
- Nw = the weight used for the NIC in Table 4
- Bv = a value for Bluetooth in Table 5
- Bw = the weight used for Bluetooth in Table 4
- Gv = a value for GPS in Table 5
- Gw = the weight used for GPS in Table 4
- Cv = a value for camera in Table 5
- Cw = the weight used for camera in Table 4
- Db = a value for the display brightness in Table 6
- Dw = the weight used for display brightness in Table 4

The last component in the algorithm is the BCS . The BCS is created by taking into account the selected value for each property of the BBCP and giving it a *property score value*. The equivalences from BBCP values to property score values are available in Table A.2 of Appendix A. A table that assigns a *combinational score values* to the combination of specific properties' values is also available in Table A.3 of Appendix A.2. Combinations are an important part of the algorithm, as they help us to detect peculiarly detrimental behavioural patterns that could be prevented. For instance, a task distribution with a lack of a strategy would be detrimental to energy consumption. Therefore, it is of the essence that the label should reflect the danger of this pattern. In addition, detecting these "energivore" (higher than usual energy consumption) combinations aid us to generate an "eco-report" to elaborate on why such combinations are detrimental, guiding software designers with it. The final BCS is given by Eq. (3), where the variables labeled from A to K represent the pertinent property score value, whose value can be found in Table A.2. Co represents the total combinational score obtained with the possible combinations of properties values in Table A.3.

$$BCS = [(Co + \sum_{i=A}^K S_i)] \quad (3)$$

where:

- S_i = property's score value in Table A.2
- Co = valid property combinational score value in Table A.3 of Appendix A.2

The algorithm includes the components explained so far to create a final Consumption Score (CS). The CS is given by Eq. (4), which produces an estimated percentage of a profile's hardware consumption, including the effect of its behavioural traits.

$$CS = (HCS \cdot HCSw + SCS \cdot SCSw) \cdot 100 + BCS \quad (4)$$

where:

- $HCSw$ = a weight value $0 \leq HCSw + SCSw \leq 1$
- $SCSw$ = a weight value $0 \leq HCSw + SCSw \leq 1$

The pseudo-code of the algorithm for a single profile is available in Algorithm 1. Once the three core components (HCS , SCS , and BCS) are obtained from the variables in the profile, the CS is obtained. The last step is to categorize (label) the estimated total percentage of hardware consumption. The categorization consists of labels from A to G, where A represents the lower hardware resource usage and G is the highest, similar to the EU's energy label, using that similarity to our advantage as people already react positively to this categorization scheme. With respect to the almost equidistant ranges that make each label eligible, we are convinced that at the current stage they suffice for providing some guidance but we do not consider these ranges to be permanently applicable. In the same fashion in which the EU's energy label has been rebalanced multiple times to close holes in their algorithms, with sufficient data a better distribution of the values for each threshold that make a label applicable could be considered. A comparison of results

among our linear, Pareto, and Gaussean scales is later included in the experimental evaluation section of this paper.

Algorithm 1 Obtaining a consumption score and a label.

Require: $BBCP$, a profile containing all the values required to generate the CS

- 1: $PCS \leftarrow$ the consumption score of the profile
- 2: $PCS = RateProfile(BBCP)$
- 3: **procedure** RATEPROFILE($BBCP$)
- 4: $HCS, SCS, BCS \leftarrow$ values from the respective profile
- 5: $Consumption\ Score \leftarrow (HCS \cdot HCSw + SCS \cdot SCSw) \cdot 100 + BCS$
- 6: **if** Consumption Score $\leq 20\%$ **then**
- 7: $label \leftarrow$ "Category A"
- 8: **else if** $20\% > Consumption\ Score \leq 30\%$ **then**
- 9: $label \leftarrow$ "Category B"
- 10: **else if** $30\% > Consumption\ Score \leq 40\%$ **then**
- 11: $label \leftarrow$ "Category C"
- 12: **else if** $40\% > Consumption\ Score \leq 50\%$ **then**
- 13: $label \leftarrow$ "Category D"
- 14: **else if** $50\% > Consumption\ Score \leq 60\%$ **then**
- 15: $label \leftarrow$ "Category E"
- 16: **else if** $60\% > Consumption\ Score \leq 70\%$ **then**
- 17: $label \leftarrow$ "Category F"
- 18: **else if** $70\% > Consumption\ Score$ **then**
- 19: $label \leftarrow$ "Category G"
- 20: **end if**
- 21: **return** Consumption Score, label
- 22: **end procedure**

As the BBCP allows software designers to describe multiple operations per profile, we created a variation of the algorithm that obtains the CS of each operation. Furthermore, a CS for each operation allows us to label the *intensity* of the hardware consumption and the behavioural characteristics of profiles. For instance, an elevated amount of "A" rated operations running simultaneously could severely impact energy consumption (a higher intensity), similarly to a G labeled operation. To label intensity, the algorithm performs a step-by-step analysis of the state of each operation, its prerequisites, and its results; all as defined by the BBCP. The evolution of the activity per step is rated with the average CS for all the active operations in each step, which is later labeled. In Fig. 1, a visual representation of a step diagram consisting of 4 operations and 3 steps can be observed. In the diagram the activity or lack of activity per operation is shown as "on" and "off" states respectively.

The variation of Algorithm 1 that supports step rating is available in Algorithm 2. This is of vital importance, as software architecture requires that the designers understand *when* each operation will take place. Such necessity is an opportunity to provide software designers with an estimation of the parallel execution of their architectures, identifying the most resource-consuming sequences and points in time. As the *StepRating* consists of the average hardware consumption of the operations at a given step, the rating can become unreliable in scenarios where the difference between the consumption of operations is too great. To address the issue, we decided to give steps where the aforementioned scenario is present the rating of its more demanding operation; this is performed by *WeightStepScore()* in Algorithm 2.

The specific steps followed by *WeightStepScore()* are the following:

- (i) We obtain the *Step Variance* (variance) among the consumption scores (Ocs) of the operations in a given step using its array of operations' consumption scores:

$$StepVariance = \frac{\sum_{k=1}^{R_l} (Ocs[k] - S_{cs})^2}{R_l} \quad (5)$$

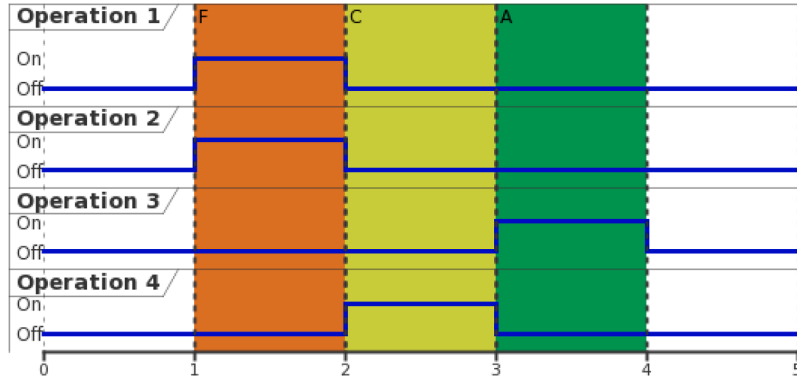


Fig. 1. An example of a step diagram created with PlantUML using the results of the algorithm.

Algorithm 2 Obtaining a consumption score and a label for a profile step using RADIANCE+.

Require: *BBCP*, a populated BBCP with all the values necessary for generating a *CS*

Require: *O_s*, the state of every operation for a given step

```

1: StepRating = RateStep(RateOperationsInStep(BBCP, Os))
2: StepScore = ScoreStep(StepRating)
3: procedure RATEOPERATIONSINSTEP(BBCP, Os)
4:   O ← all the operations in the BBCP
5:   Aocs ← an array with the CS of each operation
6:   for each o ∈ O
7:     if Os[o] is On then
8:       We obtain:
9:       HCS, SCS, BCS ← of operation [o]
10:      Ocs ← the CS of operation [o]
11:      We push the tuple (Ocs, [o]) into Aocs
12:    end if
13:  end for
14:  return Aocs
15: end procedure
16:
17: procedure RATESTEP(Aocs)
18:   RI ← length of the array Aocs
19:   StepConsumption =  $\frac{\sum_{k=1}^{RI} A_{ocs}[k][Ocs]}{RI}$ 
20:   WeightedStepRating = WeightStepScore(StepConsumption, Aocs)
21:   return WeightedStepRating
22: end procedure
23:
24: procedure SCORESTEP(StepRating)
25:   label ← obtained with the criteria of Algorithm 1
26:   return label
27: end procedure

```

where:

RI = the number of operations in the step

O_{cs} = the operations' consumption score

S_{cs} = the mean consumption score of the step

(ii) We obtain the standard deviation of the variance, *StepDeviation* (*SD*) using the *StepVariance* (*SV*), of the previous step:

$$StepDeviation = \sqrt{SV} \quad (6)$$

(iii) We decide the final *StepRating* based on the *SD*:

$$StepRating = \begin{cases} \text{highest score in } A_{ocs}, & \text{if } SD \geq SR_t \\ \text{average } O_{cs} \text{ for the step,} & \text{if } SD < SR_t \end{cases} \quad (7)$$

where:

SR_t = a deliberate threshold amount

After obtaining a *weighted Step Score* through the steps above, the step is assigned a label, and the step rating and categorization procedure is finished. In the following section, we present a series of experiments where we profile and rate (Discord, 2024), a voice, chat, and video call platform, under diverse circumstances, to observe how the current weight system's values and each component of the algorithms categorize it. In addition, we also compare the label generated with our algorithm when profiling a 3D rendering process on two different machines.

5. Experimental evaluation

The experimental evaluation of the expert algorithm consisted of 2 different sets of experiments: a set of test cases using a voice/text/chat communication app and a test case running a 3D modeling app. There were 3 main objectives to our experiments: (1) test the expert labelling algorithm with preexisting software by obtaining hardware consumption metrics under diverse circumstances; (2) test if the properties of software behaviour proposed with the BBCP appropriately affect the label, and aid the user to infer the behaviour of an existing software; and (3) determine if transferring the consumption of resources demanded by software in one device produced a different label in other hypothetical machines, and was meaningful. The premise of these experiments is that labelling a similar application to the application that software designers and developers wish to create, using our algorithm, will tell them:

- Which features of the application could consume power the most.
- What behaviours of the application can increase power consumption.
- How could the features of their application be labelled and how much do each of them matter.
- How different combinations of software behaviours can affect power consumption.
- How efficient their designs could be when executed on hardware platforms with varying configurations.

5.1. Specific tools and methods

Democratizing the adoption of software design labelling methods as our goal implies that the methods of collecting sample data should remain easy to replicate. Due to the scope of our work, precision was not of the outmost importance to us, but obtaining a baseline with a simple method of data recollection remained necessary to generate a frame of comparison that we could use to test our proposal. Specifically, the tools and methods used during the collection of data for each test case and their variation are as follows:

Table 7
Experimental test cases' samples.

Tag	Sample Name	Execution Place	Noise Suppression	Noise Level (dB)	Network Connection	Frame Rate	Image Resolution
TC1S1	Idle Sample 1	Foreground	N/A	N/A	N/A	N/A	N/A
TC1S2	Idle Sample 2	Background	N/A	N/A	N/A	N/A	N/A
TC2S1	Voice Call Sample 1	Foreground	Off	60	N/A	N/A	N/A
TC2S2	Voice Call Sample 2	Foreground	On	60	N/A	N/A	N/A
TC2S3	Voice Call Sample 1	Background	Off	60	N/A	N/A	N/A
TC2S4	Voice Call Sample 2	Background	On	60	N/A	N/A	N/A
TC3S1	Video Call Sample 1	Background	Off	60	WAN	N/A	N/A
TC3S2	Video Call Sample 2	Background	On	60	WAN	N/A	N/A
TC3S3	Video Call Sample 1	Foreground	Off	60	WAN	N/A	N/A
TC3S4	Video Call Sample 2	Foreground	On	60	WAN	N/A	N/A
TC4S1	Live Stream Sample 1	Background	N/A	N/A	LAN	30	720p
TC4S2	Live Stream Sample 2	Background	N/A	N/A	LAN	60	1080p
TC4S3	Live Stream Sample 3	Background	N/A	N/A	WAN	30	720p
TC4S4	Live Stream Sample 4	Background	N/A	N/A	WAN	60	1080p

- Power consumption recording: a smart power meter was employed during each sample to record the average system-wide power consumption of each laptop. More precisely, we used a Xiaomi Smart Plug 2. We chose this smart meter because it is available in multiple regions of the world, has an affordable price, and is consumer friendly, reducing the need of specialized hardware, which is frequently identified as a hurdle in the adoption of green software methods (Hindle, 2016). Besides, the laptops employed during the experiments ran solely on A/C power to enhance the precision of our power consumption readings.
- Hardware recording: the laptops employed during the test cases ran Windows 11 build 23H2, and PerfMon was used to record hardware consumption with a sampling rate of one sample every 15 s ($\frac{1}{60}$ Hz).
 - The network consumption employed in each profile per variation of each test case is the average of the sum of the transmission and the reception rates in Mbits.
- Repeatability and precision: 15 samples of each test-case variation were recorded to ensure repeatability and enhance the precision of our reading. Data were statistically processed to remove outliers using a Z score method. The test cases which involved user input were performed using a lightweight macro-recording software called TinyTask, ensuring repeatability of the input commands and a minimum overhead.

Although these tools and methods used in our experimental evaluation are not extremely sophisticated, they serve our purpose and facilitate the replication of the experiments.

5.2. Labelling an internet-based communication application

For the first set of experiments, consisting of 4 test cases with multiple variations, we selected *Discord* (2024) (a popular voice, chat, and video call application) as the test application. The purpose of the four tests was to observe the labels generated with our algorithm in relation to the consumption generated by each feature of Discord. The starting parameters and variations for each test case sample are available in Table 7. Discord was selected due to its popularity in the age groups 18 to 34 (Ceci, 2023), which are heavily engaged in video game playing; a relatively energy-expensive activity when compared to other computer-based activities (Mills et al., 2019). The test cases defined are suitable for our purposes, as we want to illustrate how our approach can be used to easily help developers to estimate software consumption from the design phase. Whereas other possible test cases could be defined, and more demanding experimental procedures could be used, it should be noted that we do not focus on hardware assessment.

The first test case (TC1), *idle consumption*, involved recording the baseline hardware consumption of Discord in a source machine running Windows 11, using perfmonitor and a sampling rate of 15 s, without any interference or usage other than Discord's home screen. Two samples

Table 8

Specifications of the portable computer (source machine) used for most of the experiments (Lenovo Legion Go).

Component	Specification
CPU	3.3 GHz AMD Z1 Extreme
TDP	15 W (manually set)
Storage	516 GB
RAM	16 GB
Network	2400 Mbps

of this test case were created with Discord executed in the foreground (TC1S1 in Table 7) and then in the background (TC1S2 in Table 7), to observe if a difference in consumption existed, as the algorithm does differentiate between foreground and background usage. The hardware specifications of the source machine used in all test cases are available in Table 8, the complete experimental setup for each sample and their results are publicly available as supplementary material (Larracoechea et al., 2025). The steps for carrying out the first test case consisted of the following:

1. Discord was executed and left on its home screen first in the foreground (TC1S1) and then in the background (TC1S2).
2. Windows 11 hardware resource monitor (perfmon) was left recording Discord's hardware usage in the source machine for 5 min.
3. The hardware resource consumption reported by the monitor during the test, and the behavioural traits of the application were used to characterize an operation in a BCP profile.

The second test case, *voice call* (TC2), involved recording the hardware consumption of Discord during a 10 min voice call between 2 participants connected through different networks. Only the metrics of our source machine were recorded. Two samples of this test case were performed: one with Discord running in the background and another with Discord in the foreground. Additionally, both samples were executed with noise suppression either on or off (TC2S1 to TC2S4). The purpose of sampling the differences in noise suppression "on" and "off" was to observe if it had an impact on the hardware consumption. The complete experimental setup for each sample and their results are available as well in the supplementary material (Larracoechea et al., 2025). The steps for each sample of the test case were as follows:

1. Discord was executed and left in a voice call in either the foreground (TC2S1 and TC2S2) or the background (TC2S3 and TC2S4) and either noise suppression off (TC2S1 and TC2S3) or on (TC2S2 and TC2S4) depending on the test case sample.
2. The hardware resource monitor was left recording Discord's hardware usage for 10 min.
3. A vacuum cleaner with an approximate sound level of 60 dB was left running in the background during the test while a text script was read

Table 9
Minimum system requirements for executing Dusk.

Component	Specification
Processor	2.4 GHz
RAM	2 GB
Storage	2 GB
Network	0 Mbps (offline)

aloud by a person. The vacuum cleaner served as a noise baseline to test noise suppression.

- The hardware resource consumption reported by the monitor during the test was used to characterize an operation in a BBCP.

The third test case, *video call* (TC3), involved recording the hardware consumption of Discord throughout a 10 min video call between 2 participants in separate networks, where only the source machine's consumption was recorded. Similar patterns with slight variations (TC3S1 and TC3S2) as the previous test case were conducted using similar steps. The only difference was the use of a 1080p external camera for the video call, capturing our research member reading a script.

The fourth and final test case, *video game live stream*, (TC4S1 through TC4S4) involved recording the hardware consumption of Discord for a 10 min video game stream between 2 participants, recording the consumption of the source machine only, while also executing the game being streamed. The game chosen for this test is “Dusk” a 3D shooter game. Dusk was chosen due to its low system requirements and its availability. Dusk's system requirements for Windows are available in [Table 9](#).

There are 4 samples of this test case (TC4S1 to TC4S4), each considering different live stream image resolutions and frame rates, with comparisons between 2 machines on a LAN (Local-Area Network) and two machines on different networks. The steps followed for each variation of this test case are as follows:

- Discord was executed and left in a video call with the second machine, both connected to the same LAN (TC4S1 and TC4S2) or separate networks (TC4S3 and TC4S4) depending on the test variation.
- Dusk was started and Discord was switched to live game streaming mode, with either 720p at 30 FPS (TC4S1 and TC4S3) or a 1080p resolution and 60 FPS (TC4S2 and TC4S4). The integrated webcam of the source machine was turned off.
- A member of our research team played the same level of Dusk for 5 min while the hardware resource monitor recorded Discord's consumption in the source machine.
- The hardware resource consumption recorded by the monitor was used to characterize an operation in a BBCP profile.

It should be noted that the properties *Operation Depth*, *Operation Task Distribution*, and *Operation Data Handling* override the values of *Depth*, *Data Handling*, and *Task Distribution*, as the feature sampled can change the way the application is perceived. Furthermore, disparities among samples reflect their intrinsic differences e.g.: video call samples' screen brightness is used in the foreground (TC3S1 and TC3S2) but not in the background (TC3S3 and TC3S4).

Taking TC4S1 (720p 30fps LAN live stream) as an example, the logic followed to characterize each BBCP profile for the test cases was similar to the following:

- Task Distribution:** *Centralized*, as the hardware consumption and monitoring occurs in the same machine.
- Computational criticality:** *High*, as the live stream is meant to be “live”; as close to “real time” as possible.
- Computational complexity:** Set to *High*, as we expect the constant video feed collection, compression and network casting to be a taxing operation.
- Distribution strategy:** *No*, as there is no need for it (equivalent to off, as the task distribution is set to centralized).

- Consumption rate:** *Indefinite*, as normally a live stream's duration depends entirely on user's discretion.
- Data flow behaviour:** *Regular*, as we have a clearly defined expectation of the rate at which data will be shown (30 fps); live video interruption is considered a detriment to the user.
- Data flow direction:** *Bi-directional* as the source machine was also receiving data from the “spectating” machine (audio feed).
- Data handling:** *Destroy*, as the live stream was not meant to be stored for further reproduction.
- Access Frequency:** *Irregular*, as the live stream occurs at the user's discretion.
- Depth:** *Background* as the user sees and interacts with Dusk, not with Discord.
- Dependence:** *Dependent* as it relies on Dusk to gather the data for the feed.
- Operation data handling:** *Destroy* as there are no differences between the operation and the profile it exists in.
- Operation task distribution:** *Centralized* for the same reasons as data handling.
- Operation depth:** *Background* for the same reason as the profile's depth value.
- CPU usage:** Obtained from the resource monitor as an average percentage of CPU consumption in the last 60 s of execution. Re-scaled to actual consumption in GHz.
- RAM usage:** Obtained from the resource monitor as a total working consumption in KB, re-scaled to GB; RAM consumption without the total RAM allocation.
- Storage usage:** Obtained from the resource monitor as a total in KB, re-scaled to GB.
- Network usage:** The average total bytes sent and received per second reported by the resource monitor.
- Camera:** *Off*, as it is not used during the sample.
- Screen brightness:** Set to *off* as Dusk was in the foreground.
- GPS:** *Off* as it is not required.
- Bluetooth:** *Off* as it was not required.
- NIC (WIFI):** *On* to perform the transmission of the live stream.
- Programming language:** *JavaScript* as Discord's front end is mostly programmed in TypeScript through React ([Discord Blog, 2023](#)).

As it can be seen, such a procedure can be used to infer the value for each property during the design phase of a software application by taking inspiration from existing applications of a similar type.

5.2.1. Results

We obtained a sample for each of the test cases by creating a test case profile using the BBCP and filling it with operations indicative of the setup for each test case sample; the details are available in [Larracoechea et al. \(2025\)](#). Later, [Algorithm 1](#) was implemented in RADIANCE+. Finally, we created a profile that held the hardware specifications of the source and guest machines ([Tables 8 and 12](#)). As previously mentioned, the setup and results for each sample are available in [Larracoechea et al. \(2025\)](#) and system-wide power consumption for every sample was recorded with a smart meter to determine the power consumption of our source machine (see [Table 11](#)). We modified the algorithm to repeat each sample with each laptop specification that we made available for it; this means that, once we captured the resource consumption of Discord and Dusk in the source machine, the same data were reutilized to test the resilience of the algorithm and its utility in comparative hypothetical hardware. We also increased our subject size from the source machine to 12 laptop specifications: the source machine operating at 3 different TDP settings, and the specifications of some models among the most sold laptops in the 4th quartile of 2024 ([Laptop Media, 2025](#)).

The results of the complete procedure produced relevant labels and insights into how our algorithm pondered every characteristic and variable, giving us the opportunity to include a 3D scatter plot in

All Laptops - Operation Metrics at Step 1

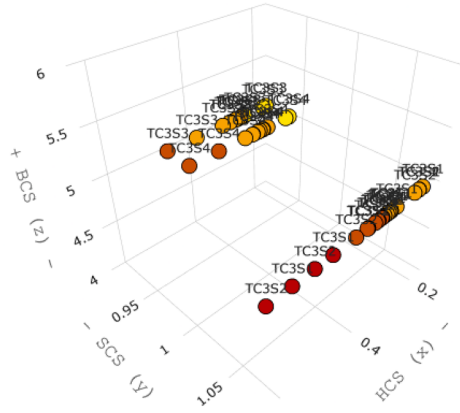


Fig. 2. Scatter plot of all the results of all samples in TC3 for all laptops.

All Laptops - Operation Metrics at Step 1

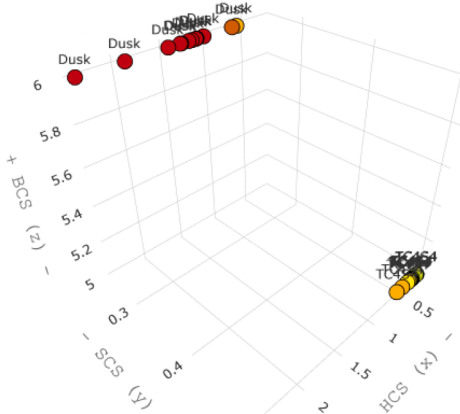


Fig. 3. Scatter plot for the simultaneous sampling (same step) of Dusk and TC4S4.

RADIANCE + to expose how the results of each sample per laptop spread out according to their HCS, SCS, and BCS. A sample of such scatter plots belonging to TC3 is available in Fig. 2.

Algorithm 2 was also implemented in RADIANCE + to categorize the simultaneous execution of TC4S4 and Dusk and obtain the score of a step with a step rating threshold (SR_i) of 15. We chose $SR_i = 15$ so that a deviation among operations' scores (SD) of 10 points (a threshold of 10) would automatically scale the step's score to the heaviest (highest O_{cs}) operation in it. We decided on a value of 15 as a round-up if the deviation among operations' scores was close to shifting into a higher label. The resulting scatter plot for the results for this algorithm is available in Fig. 3. Regarding the results, most of the laptops inherited a label comparative to the O_{cs} of Dusk. This was due to the sheer difference in magnitude between the O_{cs} of both operations in the same step, which is evidenced by the observed distance in Fig. 3.

Additionally, we used the data in Table 10 and found that a positive correlation exists between the Gross CPU efficiency $\frac{TDP}{C_a}$ of the CPU in a hardware host and the Step O_{cs} as we intended, with $r(13) = 0.96, p < .05$, hence why almost all the laptops involved (84.6 % of them) were labeled G, and the remaining laptops with a D. The complete sample setup, hardware specifications, results and labellings for every sample in TC1 through TC4 are available in Larracoechea et al. (2025). To conclude, we experimented with the thresholds that label the CS of each sample with two additional distributions: Pareto and a Gaussian bell curve. This change in the distribution of the values assigned to the thresholds changed the rigor of the categorization, as can be seen in Fig. 4.

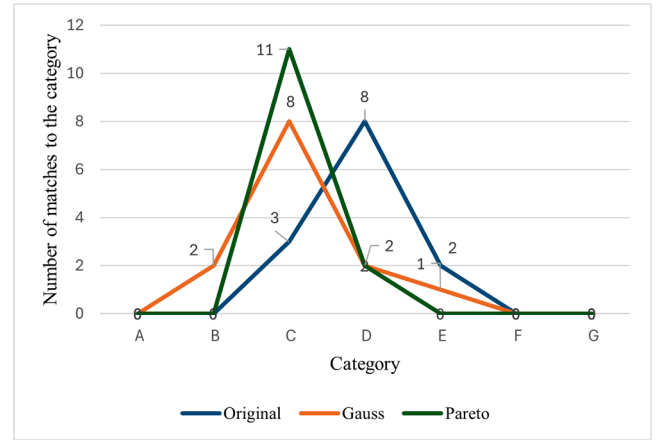


Fig. 4. Recategorization of the results obtained from TC4S4 with a rebalancing of the thresholds using Pareto and a Gaussian bell curve.

Table 10

Data summary for laptops.

Laptop Model	CPU efficiency	Step OCS	Label
HP 14 (HP 14-ep1001ns)	25	179.854	G
Apple MacBook Air 13 (2022)	6.198	99.101	G
Apple MacBook Air 15 (2024)	7.273	105.840	G
Acer Aspire 3 (A315-24P)	5.357	92.502	G
Lenovo V15 G2 IJL	5.455	93.183	G
Lenovo IdeaPad Slim 3 Chromebook	7.143	105.05	G
HP 15-7H3D6UA	5.357	92.49	G
HP 17-cn0000	5.957	96.60	G
HP 15-fd0166ns	1.765	61.32	F
Lenovo Legion Go (5W)	1.515	57.25	E
Lenovo Legion Go (15W)	4.545	86.56	G
Lenovo Legion Go (30W)	9.091	115.29	G
Alienware 13 R3 2013	16.071	147.19	G

Table 11

Recorded system-wide power consumption during each test case executed in the source machine @ 15W of TDP unless contradicted and a sample of the recorded power usage in the guest machine, all using a smart meter.

Test Case	Average power recorded (W)	CS obtained
TC1S1	17	13.57
TC1S2	17	14.428
TC2S1	15	18.310
TC2S2	15	19.196
TC2S3	15	23.301
TC2S4	15	23.071
TC3S1	19	57.32
TC3S2	19	59.020
TC3S3	16	49.234
TC3S4	17	50.191
TC4S1	17	39.708
TC4S2	17	39.017
TC4S3	17	39.150
TC4S4	17	39.686
Blender - Lenovo 5W (source)	16	19.224
Blender - Lenovo 15W (source)	35	78.058
Blender - Lenovo 30W (source)	52	126.092
Blender - Alienware 45W (guest)	68	172.654

5.3. Labelling a 3D modelling and rendering application

In addition to Discord, we also performed an experiment to categorize the hardware consumption of Blender (Foundation, 2024), an open-source 3D creation suite, while rendering a scene and generating a still image from a 3D scene. We selected Blender due to its open-source nature and repository of free test scenes. These previous and last experiments were created to demonstrate: (1) the categorization of a more

All Laptops - Operation Metrics at Step 1

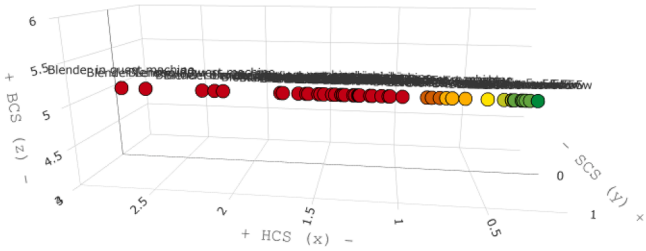


Fig. 5. Scatter plot of the results of each laptop sampled in the Blender test case.

Table 12

Specifications of the portable computer (guest machine) used for the rendering experiment with Blender.

Component	Specification
CPU	2.8 GHz (I7 7700HQ)
TDP	45W (OEM setting)
Storage	500 GB
RAM	32 GB
Network	867 Mbps

computationally expensive software operation and (2) the contrast in labels generated using two different hardware platforms. Table 7 summarizes the circumstances of the experiment surrounding each sample.

The same Algorithm 1 was used to sample each test case, with slight variations depending on the sample's demands. The test case consisted of the following:

1. The source machine was left rendering a control scene as-is (available in: <https://www.blender.org/download/demo/bundles/bundles-3.0/asset-demo-bundle-3.0-cube-diorama.zip/>), with no parameter modifications to the render settings.
2. The resource manager recorded the process from the beginning to the end of the render.
3. We created a BBCP and characterized it in a similar fashion to the previous samples to obtain a final label.
4. We repeated the steps with another machine we call the “guest machine”, whose hardware specifications can be found in Table 12. Three samples were recorded with the test machine at a TDP of 5W, 15W, and 30W respectively.

We hypothesized, for the last experiment, that the energy label would be close to the worst, a G, as the rendering process employs “ray tracing”: a technique where a collection of vectors representing a “ray of light” are scattered from a pre-defined source point in the 3d scene. The consequential bounces of the rays that go back into the source are taken as input for generating reflections (such as a reflection of a mirror) and shadows. This rendering technique is popular due to the quality of its results. Furthermore, it is the main feature in many contemporary video games which use this technology to create impressive surroundings for the players to play in.

5.3.1. Results

The data of the results for each of the samples in this test case can be found in Section 1.6 of the supplementary material (Larracochea et al., 2025). They depict a similar scenario to Dusk without additional software labeled simultaneously (a step rating). The similarities are evident by the spread data points per laptop in the scatter plot of Fig. 5, which we attribute to a positive correlation in efficiency to Ocs .

5.4. Discussion

As justified by the results, and the significant amount of additional data we present in our supplementary online material (Larracochea et al., 2025), it is manageable and informative to consider the peculiarities of a software product being executed in multiple tentative hardware hosts from a black box perspective and extrapolate the results from a single recorded sample (the guest machine) to generate labels that represent both the peculiarities of software and the appropriateness of a hardware host for execution. While the positive correlation between the CPU's gross power efficiency ratio may initially appear modest, we believe it provides meaningful support for our proposal. It highlights a valuable mechanism for promoting efficiency and greener software practices by punishing raw processing power or capacity. Moreover, we believe that delving deeper into the peculiarities of hardware would misguide our objective of labelling software (Comission, 2024b). While our study could be extended with even more experiments, the consideration of more variables, and fine-tuning of the weights for each component in our proposal, the results obtained and the recorded system-wide power consumption data available in Table 11 are already correlated by $r(18) = 0.937$, $p < .05$, and so they can be considered reliable.

However, we noticed that there are several research directions that could be approached. Firstly, as the hardware resource usage metrics are an average generated per second by the performance monitor, a study on the intensity of software usage (how long software is used over time) could be done to re-adjust or re-purpose the algorithm to consider the intensity of the usage, considering more than a single instance of a profile. We are currently exploring this direction, as the experiment with Blender demonstrated that, even though the proportional usage of the CPU for both machines remained more or less the same (around 94% with the TDP set to 15W in the source machine), the disparity between the amount of time it took to render the scene in each machine is an important metric that matters to long-term consumption of resources.

Secondly, as the hardware resource usage we measured produced a low ratio of consumption to availability, more tests could be performed under flexible hardware availability to catalog software under restrictive hardware circumstances. Especially when software depends on others, such as how Discord depends on a game to perform live game streaming. This could be done by prioritizing the operations in the profiles with the highest computational criticality over other profiles, reducing the hardware resources available for the subsequent parallel execution of profiles.

Thirdly, the weights' values we assigned to the CS (0.7 for the HCS and 0.3 for the SCS) were selected before testing, with the initial hypothesis that the change in hardware consumption would be drastic among Discord's features. This proved to be true, and we found a positive relation, which is why we maintained these values. A deeper sensitivity analysis could be performed to better understand and assign weights that better reflect the intent of the software against its instant consumption, to produce a label based on projections, for example, an *indefinite* consumption rate could increase the weight of HCS (HCS_w).

We must also highlight that the best hypothetical hardware host for every experiment was the Lenovo Legion Go @ 5W of TDP (as seen in Tables 5, 9, 13, 17, and 21 of the supplementary material (Larracochea et al., 2025)). This came partially as a surprise to us because we expected it to under-perform with such a harsh limit applied. The maximum clock speed we observed during the Blender sample @ 5W of TDP for instance was 0.55 GHz, from which it only consumed an average of 0.13% during the experiment. This sparked in us enthusiasm for devices that let knowledgeable users tailor the performance to their needs against conforming to software demands. This could lead our readers to conclude that our results are skewed by the manual setup of our source machine, but we would like to remind them that the tests were conducted with the device set at 15W of TDP and that the consumption of resources recorded with it were used as the consumption baseline and,

therefore, the baseline for cross comparison with other laptops. We are eager to see more manufacturers adopt features that cede the control over energy-hungry hardware components to the user in such a simple manner.

Finally, we recognize the relevance of incorporating insights from related fields such as energy analytics in cloud computing, process scheduling, and energy-aware AI design. While our current work focuses on hypothetical consumer hardware environments from a design-stage labelling perspective, we believe that the core ideas, particularly the black-box profiling approach and consumption-based software categorization, could be extended to cloud environments, where virtualization and dynamic scheduling decisions significantly impact energy consumption. Similarly, integrating our methodology with process scheduling strategies or AI systems that optimize for energy efficiency could unlock promising avenues for future research. We believe that the advanced features of the BBCP (Larracoechea et al., 2024a), including its support for time-restricted and shifting stochastic behaviours, already provide a solid foundation for exploring such avenues. This opens the door for us and for others who wish to adapt our approach to extend the proposal toward domains such as cloud energy analytics, process scheduling, and energy-aware AI design.

6. Conclusions and future work

In this paper, we presented our expert algorithm for categorizing the hardware consumption, behavioural characteristics, and sensor usage of prospective (and existing) software applications and their features. In addition, we proposed a variant of the first algorithm that categorizes the intensity of the hardware consumption and behaviour that prospective software could exhibit. The novelty of our proposal, compared to existing approaches—such as measuring software power consumption through direct interaction with CPU-level APIs, power meters, or specialized testbeds—lies in the ability to generate a label at the design stage of the SDLC, before any software exists, without relying on mandatory tools or intricate processes, and tightly integrated with a DSL and our custom CASE tool, RADIANCE+. This tool offers a flexible framework that can be configured and updated according to the preferences of the user and emerging recommendations in this area. Our experimental tests of the algorithm and its variant show that the labels can be obtained by inferring the behaviour of a similar software and testing its consumption of hardware resources or obtaining test data from its hardware requirements, and cross-examine alternative hardware hosts to find the most applicable hardware hosts from an efficiency point of view that uses readily-available hardware specification metrics. We also found a positive correlation between the recorded system-wide consumption and final scores of the samples that we validated our proposal with. Finally, we do not want to understate that this proposal forms part of cohesive approach for software design. The intricacy of the algorithm proposed here and the data generated with it do not necessarily imply that the process to obtain it should be cumbersome. A guide for replicating the experiments in this paper with very simple steps is available in our supplementary material (Larracoechea et al., 2025), which includes the detail data of our experiments. Besides, a website that contains additional information and other resources related to our proposal of EnergyScore is available (University of Zaragoza & Pau, 2025).

As future work, we will explore how the algorithm can be adapted to consider the evolution of prospective hardware workloads, shifts between software behaviour and software features, and the change in usage patterns over time. In addition, we will explore how scheduling algorithms that take into account the labels produced with our proposal could produce “greener” software deployments. Finally, we will consolidate our proposal for a green software design method that fosters energy saving starting from, but not limited to, the design stage of software.

CRedit authorship contribution statement

Jorge Andrés Larracoechea: Conceptualization, methodology, software, validation, formal analysis, data curation, visualization, writing – original draft preparation.; **Philippe Roose:** Conceptualization, methodology, supervision, resources, funding acquisition, writing – review and editing.; **Sergio Ilarri:** Conceptualization, methodology, writing – review and editing, visualization, supervision, resources, project administration, funding acquisition..

Data availability

Our research data is available at the following Mendeley Data Page: [10.17632/bfxp7p422w.2](https://doi.org/10.17632/bfxp7p422w.2)

Declaration of competing interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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Appendix A. Properties of the BBCP employed

Table A.1 presents the high-level properties considered by the algorithm when analyzing software behaviour and energy consumption (in the table, “Com. Cent.” is a shorthand for “Computation Centric”, “Dat. Cent.” for “Data Centric”, “Con. Cent.” for “Conduct Centric”, and “Comput.” for “Computational”). These properties are categorized into three dimensions: computation-centric, data-centric, and conduct-centric, each highlighting different aspects of software operation. From distribution strategies and computational criticality to data handling and usage patterns, the table outlines key qualitative and quantitative traits that help determine the possible energy-impact and potential optimization strategies. The combination of tags, definitions, and value ranges offers a structured overview to guide profiling and decision-making.

Table A.1

High-level properties used by the algorithm.

Tag and property	Com. Cent.	Dat. Cent.	Con. Cent.	Definition	Possible values
A: Task distribution	✓	×	×	This qualitative property defines if the computation that software performs has to be always executed in the same computational entity or it can be distributed among several others. It also helps us to understand where energy is going to be consumed.	Centralized, Distributed
B: Distribution strategy	✓	×	×	This qualitative property defines if the user has a distribution strategy planned for distributed tasks.	Yes, No
C: Comput. criticality	✓	×	×	This qualitative property is meant to define if the computational results are tied to a time constraint. Knowing there is a time constraint, requiring the computationally fastest and physically closest host.	Low, Medium, High
D: Comput. complexity	✓	×	×	This qualitative and quantitative property establishes an amount of computation required to accomplish the goal of a software or software component.	Low, Medium, High
E: Data flow behaviour	×	✓	×	This qualitative property refers to the consistency of the data flow where consumption of data can be, or not, interrupted. It helps us to identify if intermittent energy management strategies can be applied.	Regular, Irregular
F: Data flow direction	×	✓	×	Complementary to the previous property, this qualitative property defines whether data flows from or to the software or from and to the software. It helps us to identify a relationship of energy consumption between software and its components, as well as other software units.	Unidirectional, Bidirectional
G: Data handling	×	✓	×	This qualitative property establishes what is done to data after the software consumes it. This helps us to assess the storage consumption of software.	Keep, Destroy, Store and broadcast
H: Access frequency	×	×	✓	This qualitative property is meant to define how often the software is used. It helps us to understand the predictability of its usage.	Regular, Irregular
I: Consumption rate	×	×	✓	This qualitative property complements the previous one (access frequency). It defines if the usage of the software is well defined or undefined; it can also be understood as “how long” whereas access frequency is “how often”.	Definite, Indefinite
J: Depth	×	×	✓	This property defines an execution level for the software fragment you are profiling within the system the application is executed in. It also allows us to know if energy management strategies can be applied to the service without detriment to the user. In other words, this property allows us to determine if we can apply energy management strategies to the software you are currently profiling without affecting the user.	Foreground, Background
K: Dependence	×	×	✓	This property establishes whether the software profile is subject to any dependency relationship with other software or profiles.	Dependee (other entities depend on it), Dependant, Independent

A.1. BCS-equivalent values per BBCP property

Table A.2 details the reference values assigned to each possible value of the high-level properties identified earlier. These reference values, ranging from 0 to 1, represent the relative impact or relevance of a property value in terms of energy consumption or man-

agement complexity. Higher values indicate a greater potential influence. The table also outlines any pre-requisites for a given value to apply and provides justifications based on empirical evidence or real-world scenarios. This mapping enables the algorithm to translate qualitative characteristics into a quantifiable framework for energy-aware profiling.

Table A.2

Property score value assigned to the possible value of each property.

Tag and Property	Property value	Score (0–1)	Pre-requisite	Justification
A0 Task distribution	Centralized	0.5	None	We decided to leave the score value as a 0.5 because we are unaware if a strategy for producing green software will be employed or not.
A1 Task distribution	Distributed	1	None	We assigned a score value of 1 due to the complexity of managing a distributed architecture, as software units (such as microservices) are volatile in behaviour, availability, cost, and quality of service (Caporuscio et al., 2020). The aforementioned reasons could produce a computational management and energy consumption overhead of attempting to use such an architecture, especially without previous experience. Recently, Amazon cited some of the previous challenges as motivators for switching from a distributed microservices architecture to a monolith application (Tech, 2024).
B0 Distribution strategy	Yes	0	None	The score value of this property was set to 0 as its purpose is to provide a combinational score; a score created from the combination of 1 or more values.
B1 Distribution strategy	No	1	None	We punish the lack of distribution strategy as it could harm the energy consumption of hardware in multiple locations with no holistic vision of how it will be consumed.
C0 Computational criticality	Low	0.3	None	An elevated priority superposes a profile over others, with the possibility of it demanding more resources and, therefore, consuming more energy or impacting the possible quality of service of other software.
C1 Comput. criticality	Medium	0.6	None	
C2 Comput. criticality	High	1	None	
D0 Comput. complexity	Low	0.3	None	
D1 Comput. complexity	Medium	0.6	None	The score value increases with the increase in the demand of computational resources.
D2 Comput. complexity	High	1	None	
E0 Data flow behaviour	Regular	0.8	None	
E1 Data flow behaviour	Irregular	0.4	None	As there is a constant exchange of data, fewer opportunities to apply energy-saving strategies are present, furthermore, a constant data exchange means that there is a constant consumption of energy involved in transmitting and processing it. As we previously stated, the energy consumption of network hardware is one of the biggest power hogs, the score value is meant to reflect this.
F0 Data flow direction	Unidirectional	0.5	None	As the value of this property represents the opposite of the previous value, we reduced its score value without bringing it down to 0, as data exchange is still a relevant variable for categorizing consumption.
F1 Data flow direction	Bidirectional	1	None	We decided that the score value was appropriate as data is sent, not exchanged, creating a weak relationship of energy consumption among software entities.
G0 Data handling	Keep	0.5	Task distribution is set to distributed	In contradiction, to the property value above, this property value establishes a strong relationship of energy consumption to maintain the communication among software entities. Hence the score value. Cloud-based applications such as Google Meet or GeForce Now are good examples of this, as a constant recollection of input from the user is sent to the cloud and a stream of data is constantly sent from the cloud to the user, creating a feedback loop.
G1 Data handling	Destroy	0	None	Keeping incoming data and generated data produces hardware consumption. The score value was chosen to reflect the increasing energy consumption of cloud storage (Pesce, 2021), as writing to a local storage device is far more efficient (Magazine, 2017) and the energy consumption of updated network transmission infrastructures, such as 5G is still unknown (Kamiya, 2021).
G2 Data handling	Store and broadcast	0.5	Task distribution is set to distributed and distribution strategy is set to yes	Destroying data after its consumption prevents the usage of storage.
H0 Access frequency	Regular	0	None	Storing data to become a provider of it, coupled with a distribution strategy, can benefit the energy consumption by becoming the closest source of data to other devices. A good example of this is CDNs (Content-Delivery Networks). Broadly speaking, CDNs allow frequently accessed data to be replicated in geographically distributed servers responsible for delivering it to the closest users. Even though a similar strategy could be replicated using devices as data nodes, a lot of variables involved in keeping the data up to date could negatively or positively impact energy consumption (Bianco et al., 2017).
H1 Access frequency	Irregular	0.5	None	A regular task frequency aids in predicting when strategies and deployments could be employed.
I0 Consumption rate	Definite	0	None	An unpredictable frequency means that software could be consumed at any point in time, making usage chaotic.
I1 Consumption rate	Indefinite	0.1	None	We can predict exactly for how long software will be used when it is solicited.
				We cannot predict exactly for how long software will be used when solicited.

Table A.2
Continued.

J0 Depth	Foreground	0.5	Task distribution must be set to centralized	The display is needed to use this software and it cannot be delegated.
J1 Depth	Background	0	Task distribution can be either centralized or distributed	The display is not employed.
K0 Dependence	Dependee	0.5	None	Other software depends on the software being profiled, establishing an energy consumption relationship elsewhere.
K1 Dependence	Dependant	0.5	None	
K2 Dependence	Independent	0	None	No energy relationship with other software is present.

Table A.3

Possible combinations of the properties used by the BCS and their combinational score values.

Tag	Combination	Score	Justification	Logical effects
C1	$A1 \wedge B1$	3	If the task distribution is distributed, we penalize not having a distribution strategy, as network consumption to distribute the tasks would produce the risk of overloading other devices.	Sets the score of A1 and B1 to 0
C2	$A1 \wedge B0$	-1	Applying a proven deployment or distribution strategy should allow for further energy savings. With this in mind we can possibly affect the energy score (reduce the score) to support the user's intentions of producing green software.	Sets the score of A1 to 0
C3	$C2 \wedge E0$	2	Contrary to the previous combination, a regular flow of data means that, now that software components are distributed in a network more network usage (among devices) is required. Therefore, energy consumption is unpredictable.	The scores of A1 and E0 are set to 0 C2 is invalidated (not applicable)
C4	$C2 \wedge E1 \wedge G2$	-2	A decentralized task distribution in addition to a strategy for distributing tasks and a strategy for broadcasting data could benefit (lower) the energy consumption by obtaining the most frugal execution and creating an easier to reach data node.	The scores of A1, E1 and G2 are set to 0 C2 is invalidated (not applicable)
C5	$C2 \wedge E1 \wedge G1$	-1	A decentralized task distribution in addition to a strategy for distributing tasks could benefit (lower) the energy consumption by obtaining the most frugal execution. A lack of data distribution strategy is not as beneficial.	The scores A1, E1 and G1 are set to 0 C2 is invalidated (not applicable)

A.2. BCS-equivalent values of combined BBCP properties

Table A.3 presents selected combinations of high-level property values used in the BCS (behavioural Consumption Score) and their associated combinational reference values. These combinations capture interactions between properties that, when occurring together, significantly influence the overall energy profile—either positively or negatively. Each combination is assigned a reference value and accompanied by a justification that explains its impact on energy consumption, along with the logical effects on individual property scores. This mechanism allows the scoring model to account for interdependencies between properties and supports more nuanced energy-aware assessments.

Supplementary material

Supplementary material associated with this article can be found in the online version at [10.1016/j.eswa.2025.130046](https://doi.org/10.1016/j.eswa.2025.130046)

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